



USB3.0 CCD camera series

# XIMea

### 1. Introduction

### 1.1. About This Manual

Dear customer.

Thank you for purchasing a product from XIMEA.

We hope that this manual can answer all your questions, but should you have more, or if you wish to claim a service or warranty case, please contact your local dealer or refer to the XIMEA Support on our website:

### www.ximea.com/support

The purpose of this document is to provide a description of the XIMEA xiD-Series cameras and to describe the correct way to install related software and drivers and run it successfully. Please read this manual thoroughly before operating your new camera for the first time. Please follow all instructions and observe the warnings.

This document is subject to change without notice.

### 1.2. About XIMEA

XIMEA is one of the worldwide leaders for innovative camera solutions with a 25-year history of research, development and production of digital image acquisition systems. Based in Slovakia, Germany and the US and with a global distributor network, XIMEA offers their cameras worldwide. In close collaboration with customers XIMEA has developed a broad spectrum of technologies and cutting-edge, highly competitive products.

XIMEA's camera centric technology portfolio comprises a broad spectrum of digital technologies, from data interfaces such as USB 2.0, USB 3.1 and PCle to cooled digital cameras with CCD, CMOS and sCMOS sensors, as well as X-ray cameras.

XIMEA has three divisions – generic machine vision and integrated vision systems, scientific imaging and OEM/custom.

XIMEA cameras find use in many industrial applications, such as motion control, robotics, or quality control in manufacturing. The broad spectrum of cameras also includes thermally stabilized X-ray cameras, and specialty cameras for medical applications, research, surveillance and defense.

### 1.2.1. Contact XIMEA

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Support https://www.ximea.com/support/wiki/allprod/Contact\_Support

### 1.3. Standard Conformity

The xiD cameras have been tested using the following equipment:

- CCD Camera with lens AZURE Photonics, AZURE-1620MX5M 4/3" 16mm C-Mount,
- Notebook HP EliteBook 8570p, Windows 7 Professional 64-bit, SN: 5CB22201WQ,
- USB 3.0 cable, with thumbscrews on Micro B connector, type CBL-U3-3M0, 3 m long,
- MD series sync cable, thread lock, 8-poles: ground, 2xTrigger In, 3 m long.

**Warning**: Changes or modifications to the product may render it ineligible for operation under CE, FCC or other jurisdictions. XIMEA recommends using the above configuration to ensure compliance with the following standards:

### 1.3.1. CE Conformity



The xiD cameras described in this manual comply with the requirements of the

EC EMC Directive 2014/30/EU electromagnetic compatibility of equipment

### 1.3.2. For customers in the US: FCC Conformity



The xiD cameras described in this manual have been tested and found to comply with Part 15 of the FCC rules, which states that:

Operation is subject to the following two conditions:

- This device may not cause harmful interference, and
- This device must accept any interference received, including interference that may cause undesired operation.

This equipment has been tested and found to comply with the limits for Class A digital device, pursuant to part 15 of the FCC rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses and can radiate radio frequency energy and, if not installed and used in accordance with the instruction manual, may cause harmful interference to radio communications. Operation of this equipment in a residential area is likely to cause harmful interference in which case the users will be required to correct the interference at their own expense.

You are cautioned that any changes or modifications not expressly approved in this manual could void your authority to operate this equipment under above jurisdictions. The shielded interface cable recommended in this manual must be used with this equipment in order to comply with the limits for a computing device pursuant to Subpart J of Part 15 of FCC Rules.

### 1.3.3. For customers in Canada

The xiD cameras comply with the Class A limits for radio noise emissions set out in Radio Interference Regulations.

### 1.3.4. RoHS Conformity



The xiD cameras comply with the requirements of the RoHS (Restriction of Hazardous Substances) Directive 2011/65/EU.

### 1.3.5. WEEE Conformity



The xiD cameras comply with the requirements of the WEEE (waste electrical and electronic equipment) Directive 2012/19/EU.

### 1.3.6. GenlCam GenTL API



XIMEA General Terms &

Conditions

GenlCam standard transport layer interface, grabbing images. GenlCam/GenTL provides an agnostic transport layer interface to acquire images or other data and to communicate with a device. Each XIMEA camera can be GenTL Producer.

### Helpful Links 1.4.

http://www.ximea.com/ Ximea Homepage https://www.ximea.com/en/usb3-vision-camera/usb3-ccd-cameras-xid xiD product page https://www.ximea.com/support/wiki/xid/CCD\_with\_USB\_30\_cameras\_xiD support page \_xiD https://www.ximea.com/support/documents/4 xiAPI stable versions download https://www.ximea.com/support/documents/14 xiAPI beta versions download http://www.ximea.com/support/wiki/allprod/Frequently Asked Questions Frequently Asked Questions http://www.ximea.com/support/wiki/allprod/Knowledge Base **Knowledge Base** http://www.ximea.com/support/projects/vision-libraries/wiki Vision Libraries http://www.ximea.com/en/products/register XIMEA Registration http://www.ximea.com/support/wiki/allprod/XIMEA\_Live\_Support XIMEA Live Support http://www.ximea.com/en/corporate/generaltc



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# 2. xiD Camera Series



Figure 2-1 xiD camera.

# 2.1. What is xiD

xiD [ksi-di: or sai-di:] cameras with newest Sony EXview HAD CCD II sensors (ICX674, ICX694, ICX814, ICX834) combined with USB 3.0 interface - designed with consideration for the Scientific applications:

- Sony ICX with "EXview HAD CCD II" pixel technology
- Passive cooling of sensor
- Improved light efficiency for NIR spectrum
- Global shutter with interline transfer
- 1, 2 or 4 tap readout with 14 bit ADC
- Lowest power consumption

The board-level versions include a system integration guide for designing an optimal heatsink and enclosure. Due to the implementation of a 4 tap readout, this series supports the maximum frame rates that the sensors deliver

Off the shelf hardware can be used for camera to computer interfaces, but testing was limited to the items discussed in this manual. See section 3.10 - 3.14 for materials needed to interface your camera to the computer.

# 2.2. Advantages

| Quality components               | Sony "EXview HAD CCD II" sensors delivering 2.8, 6.1, 9.1 and 12 Mpix      |
|----------------------------------|--|
| Fastest                          | Highest speed using full 4 TAP readout potential                           |
| Industry standard interface      | Compliant with USB 3.0 SuperSpeed specification                            |
| Versatile mini camera            | Subtle 60 x 60 x 39 mm, 320 grams  |
| Power                            | Lowest power consumption starting at 3 Watt, bus powered with USB3 cable   |
| Cool                             | Minimal heat dissipation with passive COOLing                              |
| Compatibility                    | USB 3.0 support for Windows, Linux, Mac OSX (planned ARM) and 30 Libraries |
| Connectivity and Synchronization | Programmable opto-isolated input and output, 3 status LEDs                 |
| Bandwidth potential              | 5Gb/s interface up to 400Mpix/s data throughput                            |
| Software interfaces              | GenlCam / GenTL and highly optimized xiAPI SDK for Image Processing        |
| High grade                       | Quality class of sensors is combined with specially selected IR filters    |
| Easy deployment                  | Range of accessories and widest hardware and software interoperability     |
| Highly Customizable              | Available in Board level   |

table 2-1, advantages

# 2.3. xiD Camera Applications

- Astronomy and Astrography
- Factory and industry automation where high resolution or NIR is required
- Ophthalmology and Retinal imaging
- Material and Life science
- Medical Imaging and Light Microscopy
- Pharmaceutics and Healthcare
- Robotic Welding
- Industrial Automation, Machine Vision, Photovoltaics
- Intelligent Transportations Systems (ITS)
- Open road tolling and Traffic monitoring
- ANPR / ALPR
- Biometrics
- Gauging, Metrology
- Defect detection, OCV / OCR
- · Astronomy, Astrography where long exposure times are needed
- Cell Biology, sorting
- Semiconductor wafer and bonding scanning
- Flat panel inspection
- Dentistry, Dermatology
- Print and Film scanning
- Laser beam profiling
- Histology, Pathology
- Fluorescence
- Low light

### 2.4. Common features

| Sensor Technology                | CCD, Global shutter   |
|----------------------------------|---|
| Acquisition Modes                | Continuous, software and hardware trigger, fps, triggered exposure and burst,   |
| Partial Image Readout            | ROI, Binning modes supported up to binning 5x5  |
| Image data formats               | 8, 10, 12 or 14 bit RAW pixel data  |
| SDK/API                          | programmable with C++ and C#  |
| Color image processing           | Host based de-Bayering, sharpening, Gamma, color matrix, true color CMS   |
| Hot/blemish pixels correction    | On camera storage of more than 5000 pixel coordinates, host assisted correction   |
| Auto adjustments                 | Host based auto white balance, auto gain, auto exposure   |
| Flat field corrections           | Host assisted pixel level shading and lens corrections  |
| Image Data and Control Interface | USB 3.0 standard Micro B  |
| General Purpose I/O              | 1x opto-isolated input, 1x opto-isolated output, 1x magnetically insulated input and 1x magnetically isolated output, 3X user configurable LEDs |
| Signal conditioning              | Programmable debouncing time  |
| Synchronization                  | Hardware trigger input, software trigger, exposure strobe output, busy output,  |
| Housing and lens mount           | Standard C-mount. Available options are board level   |
| Power requirements <sup>1</sup>  | 3.6-5.2W, supplied via USB 3.0 interface  |
| Environment                      | Operating 0°C to 50°C on housing, RH 80% non-condensing, -30°C to 70°C storage Ingress Protection: IP40   |
| Operating systems                | Windows 7 SP1 (x86 and x64), Windows 10 (x86 and x64), Linux Ubuntu, MacOS 10.8   |
| Software support                 | xiAPI SDK, adapters and drivers for various image processing packages   |
| Firmware updates                 | Field firmware updatable trough xiCOP   |

table 2-2, common features

Note: 1) Power consumption is model specific



# 2.5. Model Nomenclature

Part number convention for the different models:

### xiD family

### MDxxxyU-zz-0PT

MD xiD family name

**xxx:** Resolution in 0.1 MPixel. E.g. 2.8 MPixel Resolution: xxx = 028

**y:** y=C: color model

y=M: black & white model

U: Uncooled

**zz:** Vendor of the sensor

zz = SY: SONY

[-OPT]: Options

OPT = BRD: board level camera

# 2.6. Models Overview, sensor and models



| Model      |       | Sensor         | Resolution  | Pixel<br>size | ADC<br>[bit] | DR      | Sensor<br>diagonal | FPS <sup>1</sup> |
|------------|-------|----------------|-------------|---------------|--------------|---------|--------------------|------------------|
| MD028MU-SY | b/w   | SONY ICX674ALG | 1934 x 1456 | 1.51.um       | 14           | 68.6 dB | 11 mm              | 56.9             |
| MD028CU-SY | Color | SONY ICX674AQG | 1934 X 1430 | 4.54 µm       | 14           | 00.0 UD | 1 1 111111         | 50.9             |
| MD061MU-SY | b/w   | SONY ICX694ALG | 2754 x 2204 | 1.51.um       | 14           | 68.7 dB | 16 mm              | 28.4             |
| MD061CU-SY | Color | SONY ICX694AQG | 2704 X 2204 | 4.54 μm       | 14           | 00.7 UD | 10 111111          | 20.4             |
| MD091MU-SY | b/w   | SONY ICX814ALG | 2204 v 2700 | 2 60 um       | 14           | 64.7 dB | 16 mm              | 19.5             |
| MD091CU-SY | Color | SONY ICX814AQG | 3384 x 2708 | 3.69 µm       | 14           | 04.7 UD | 10 111111          | 19.5             |
| MD120MU-SY | b/w   | SONY ICX834ALG | 4242 x 2830 | 0.1 um        | 14           | CO E AD | 15.8               | 15.0             |
| MD120CU-SY | Color | SONY ICX834AQG | 4242 X 2030 | 3.1 µm        | 14           | 63.5 dB | mm                 | 15.3             |

table 2-3, models overview

Note: 1) Full resolution, 4-TAP readout 14 bit/pixel

# 2.7. Options

Most models are available as a board level version, please inquire



# 2.8. Accessories

The following accessories are available (short list):

| Item P/N                    | Description   |
|-----------------------------|---|
| CBL-U3-1M0                  | 1.0m USB 3.0 cable, micro B connector on camera side        |
| CBL-U3-3M0                  | 3.0m USB 3.0 cable, micro B connector on camera side        |
| CBL-U3-3M0-ANG              | 3.0m USB 3.0 cable, angled micro B USB3 connector           |
| CBL-U3-5M0                  | 5.0m USB 3.0 cable, micro B connector on camera side        |
| MECH-60MM-BRACKET-T         | xiD series tripod mounting bracket                          |
| CBL-MD-PWR-SYNC-3M0         | 3.0 Trigger/Sync I/O cablel                                 |
| U31PE1G3-V1-X2 <sup>1</sup> | PCI express adapter, 2x USB 3.1 ports asmedia ASM1142, xHCI |
| U3PE-FL1100-X4 <sup>1</sup> | PCI express adapter, 4x USB 3.0 ports, PCIe x4 slot         |
| CBL-U3-3M0-YP               | Y-Shaped Power USB3 Cable 3.0m, thumbscrews                 |
| CBL-U3-3M0-YP-UA            | Y-Shaped Power USB3 Cable 3.0m, Up Angle thumbscrews        |
| CBL-U3-5M0-YP               | Y-Shaped Power USB3 Cable 5.0m, thumbscrews                 |
| CBL-U3-5M0-YP-UA            | Y-Shaped Power USB3 Cable 5.0m, Up Angle, thumbscrews       |

table 2-4, accessories

Note: 1)For more information please visit:

https://www.ximea.com/support/projects/usb3/wiki/USB\_3\_Host\_Adapters

# 3. Hardware Specification

# 3.1. Power Supply

The xiD cameras are powered via the USB Micro-B. The input voltage is 5 V DC.

The power consumption is 3.8 - 5W depending on the xiC model.

Power supply, via USB system connector:

- 5 V (nominal)
- 4.45 V to 5.5 V (at the camera connector)

For stable operation of the cameras with higher power consumption (such as model 091 and 120) it is advised to use USB3.0 cable with secondary USB connector CBL-U3-3M0-YP or similar to provide enough power to the camera

# 3.2. General Specification

### 3.2.1. Environment

| Description  | Value         |
|--|---------------|
| Optimal ambient temperature operation              | +10 to +25 °C |
| Ambient temperature operation                      | +0 to +50 °C  |
| Ambient temperature for storage and transportation | -30 to +70 °C |
| Relative Humidity, non-condensing                  | 80 %          |

table 3-1, environment

Housing temperature must not exceed  $+65^{\circ}$ C. The following parameters are not guaranteed if the camera is operated outside the optimum range:

- Dark current
- Dynamic Range
- Linearity
- Acquisition
- Readout noise
- S/N ratio
- Durability

### 3.2.2. Firmware / Host driver / API features

| Description  | Value         |
|--|---------------|
| Interpolation  | Proprietary   |
| White balance coefficients ranges                        | 0.0 to 3.9    |
| Sharpness filter   | -400 to 400 % |
| Gamma  | 0.3 to 1.0    |
| Full color correction matrix (3+1)x3 coefficients ranges | -3.9 to 3.9   |

table 3-2, firmware / API features

More details on API/SDK features are available at XIMEA support pages: http://www.ximea.com/support

### 3.3. Lens Mount

The xiD cameras C-mount lenses.

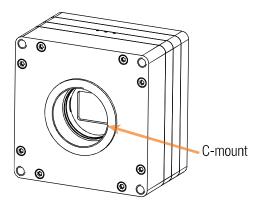


figure 3-1, xiD lens mount

# 3.4. Mounting points

Mounting points available to the customer are shown below. All are M4 thread.

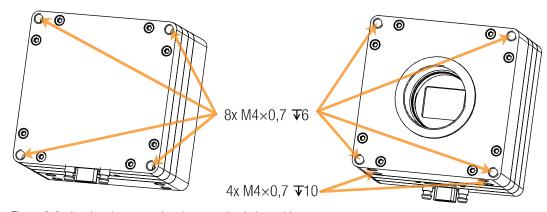


Figure 3-2, drawing demonstrating the mounting hole positions.

# 3.5. Optical path

A filter glass is part of the optical path of the camera. This glass is placed on a layer of silicone, to keep dust out of the camera, but not glued. Operating the camera without a lens mount is not intended and can lead to dropping out of the filter glass and the entry of dust.

Do not use compressed air to clean the camera as this could push dust into the sensor chamber.

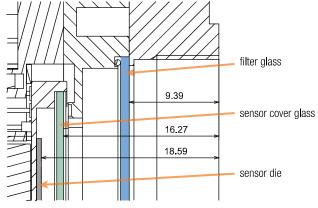


figure 3-3, Optical path section



# 3.5.1. Monochrome and near infrared extended camera models

| Used filter brand | BK7 AR2x               |  |
|-------------------|------------------------|--|
| Thickness         | 1.0±0.1 mm             |  |
| Coating           | Anti-reflex both sides |  |

table 3-3, monochrome camera - filter glass parameter

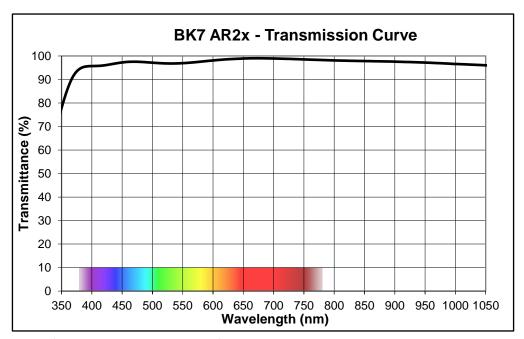


figure 3-4, monochrome camera - filter glass transmission curve

# 3.5.2. Color camera models

| Used filter brand | ICR650                 |
|-------------------|------------------------|
| Thickness         | 1.0±0.1 mm             |
| Coating           | Anti-reflex both sides |

table 3-4, color camera - filter glass parameter

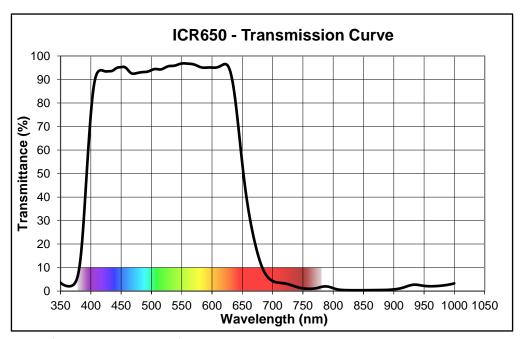


figure 3-5, color camera - filter glass transmission curve



# 3.6. Model Specific Characteristics

# 3.6.1. MD028xU-SY

# 3.6.1.1. Sensor and camera parameters

| xiD model                      |                         | MD028CU-SY MD028MU-SY |                     |  |
|--------------------------------|-------------------------|-----------------------|---------------------|--|
| Sensor parameter               |                         |                       |                     |  |
| Part number                    |                         | ICX674AQG             | ICX674ALG           |  |
| Color filter                   |                         | RGB Bayer mosaic      | None                |  |
| Туре                           |                         | С                     | CD progressive scan |  |
| Pixel Resolution (W x H)       | [pixel]                 |                       | 1934 x 1456         |  |
| Active area size (W x H)       | [mm]                    |                       | 8.78 x 6.61mm       |  |
| Sensor diagonal                | [mm]                    |                       | 11 mm               |  |
| Optical format                 | [inch]                  |                       | 2/3"                |  |
| Pixel Size                     | [µm]                    |                       | 4.54µm              |  |
| ADC resolution                 | [bit]                   |                       | 14                  |  |
| FWC                            | [ke-]                   |                       | 20                  |  |
| Dynamic range                  | [dB]                    |                       | 68.1                |  |
| SNR Max                        | [dB]                    |                       | 43.7                |  |
| Conversion gain                | [e-/LSB <sub>14</sub> ] |                       | 1.34                |  |
| Dark noise                     | [e-]                    | 8.8                   |                     |  |
| Dark current                   | [e-/s]                  |                       | TBD                 |  |
| DSNU                           | [e-]                    | TBD                   |                     |  |
| PRNU                           | %                       | TBD                   |                     |  |
| Linearity                      | [%]                     | <1                    |                     |  |
| Micro lenses                   |                         | Yes                   |                     |  |
| Camera parameters              |                         |                       |                     |  |
| Digitization                   | [bit]                   | 14                    |                     |  |
| Supported bit resolutions      | [bit/pixel]             |                       | 8, 10, 12, 14       |  |
| Exposure time (EXP)            |                         |                       | 0.047ms - 712 s     |  |
| Variable Gain Range (VGA)      | [dB]                    |                       | -3 – 41             |  |
| Refresh rate (MRR)             | [fps]                   | 56.9                  |                     |  |
| Power consumption <sup>1</sup> |                         |                       |                     |  |
| Stand by                       | [W]                     | 0.76                  |                     |  |
| Maximum                        | [W]                     | 3.6                   |                     |  |
| Dimensions/Mass                |                         |                       |                     |  |
| height                         | [mm]                    | 60                    |                     |  |
| width                          | [mm]                    | 60                    |                     |  |
| depth                          | [mm]                    | 37.2                  |                     |  |
| mass                           | [g]                     |                       | 320                 |  |

table 3-5, MD028xU-SY, sensor and camera parameters

Notes: 1) Measured at 4TAP/52MHz readout



| Binning(readout) | TAPs | MD028CU-SY       | MD028MU-SY | Pixels      | Bit/px | fps   |
|------------------|------|------------------|------------|-------------|--------|-------|
| 1x1              | 1    | Color            | B/W        | 1936 x 1456 | 14     | 15.8  |
| 2x2              | 1    | B/W¹             | B/W        | 966 x 726   | 14     | 28.7  |
| 3x3              | 1    | B/W¹             | B/W        | 644 x 482   | 14     | 39.3  |
| 4x4              | 1    | B/W <sup>1</sup> | B/W        | 482 x 360   | 14     | 48.3  |
| 5x5              | 1    | B/W¹             | B/W        | 384 x 288   | 14     | 55.9  |
| 1x1              | 2    | Color            | B/W        | 1936 x 1456 | 14     | 28.7  |
| 2x2              | 2    | B/W <sup>1</sup> | B/W        | 966 x 726   | 14     | 48.2  |
| 3x3              | 2    | B/W¹             | B/W        | 644 x 482   | 14     | 62.5  |
| 4x4              | 2    | B/W¹             | B/W        | 482 x 360   | 14     | 73.3  |
| 5x5              | 2    | B/W <sup>1</sup> | B/W        | 384 x 288   | 14     | 81.5  |
| 1x1              | 4    | Color            | B/W        | 1936 x 1456 | 14     | 56.9  |
| 2x2              | 4    | B/W¹             | B/W        | 966 x 726   | 14     | 95.0  |
| 3x3              | 4    | B/W <sup>1</sup> | B/W        | 644 x 482   | 14     | 122.8 |
| 4x4              | 4    | B/W¹             | B/W        | 482 x 360   | 14     | 143.3 |
| 5x5              | 4    | B/W <sup>1</sup> | B/W        | 384 x 288   | 14     | 159.0 |

table 3-6, MD028xU-SY, standard readout modes

Notes: 1) Image data contains some part of color information depending on binning

# 3.6.1.2. Quantum efficiency curves [%]

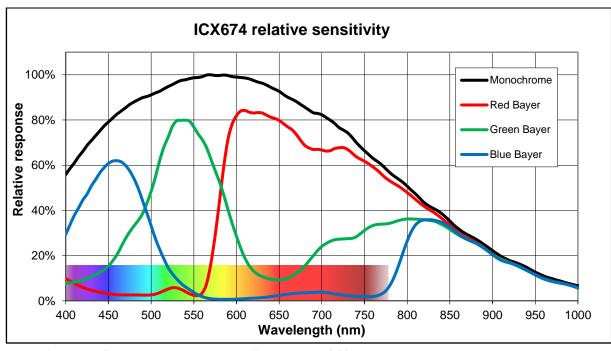


figure 3-6, ICX674-mono, color; quantum efficiency curve, ©SONY

# 3.6.1.3. Dimensional drawings MD028xU-SY

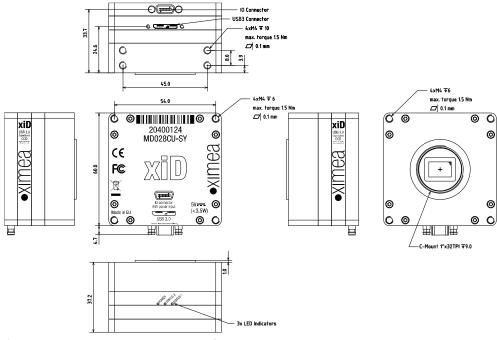


figure 3-7, dimensional drawing MD028xU-SY

### 3.6.1.4. Referenced documents

ICX674 Datasheets, Sony Corp.

### 3.6.1.5. Sensor features

| feature    | Note   |  |
|------------|--|--|
| Binning    | Yes, up to 5x5                                     |  |
| Skipping   | No   |  |
| ROI        | Vertical cropping results in increased read speed. |  |
| HW Trigger | Trigger without overlap                            |  |
| HDR        | Not available                                      |  |

table 3-7, sensor features available

# 3.6.2. MD061xU-SY

# 3.6.2.1. Sensor and camera parameters

| xiD model                      |                         | MD061CU-SY MD061MU-SY |             |  |
|--------------------------------|-------------------------|-----------------------|-------------|--|
| Sensor parameter               |                         |                       |             |  |
| Part number                    |                         | ICX694AQG             | ICX694ALG   |  |
| Color filter                   |                         | RGB Bayer mosaic      | None        |  |
| Туре                           |                         | CCD progr             | essive scan |  |
| Pixel Resolution (W x H)       | [pixel]                 | 2752                  | x 2204      |  |
| Active area size (W x H)       | [mm]                    | 12.5 x                | 10.0mm      |  |
| Sensor diagonal                | [mm]                    | 16                    | mm          |  |
| Optical format                 | [inch]                  | -                     | 1"          |  |
| Pixel Size                     | [µm]                    | 4.5                   | 4μm         |  |
| ADC resolution                 | [bit]                   | 1                     | 4           |  |
| FWC                            | [ke-]                   | 19                    | 9.5         |  |
| Dynamic range                  | [dB]                    | 67                    | 7.4         |  |
| SNR Max                        | [dB]                    | 43                    | 3.5         |  |
| Conversion gain                | [e-/LSB <sub>14</sub> ] | 1.28                  |             |  |
| Dark noise                     | [e-]                    | 8.9                   |             |  |
| Dark current                   | [e-/s]                  | TI                    | BD          |  |
| DSNU                           | [e-]                    | TBD                   |             |  |
| PRNU                           | %                       | TBD                   |             |  |
| Linearity                      | [%]                     | <1                    |             |  |
| Micro lenses                   |                         | Yes                   |             |  |
| Camera parameters              |                         |                       |             |  |
| Digitization                   | [bit]                   | 1                     | 4           |  |
| Supported bit resolutions      | [bit/pixel]             | 8, 10,                | 12, 14      |  |
| Exposure time (EXP)            |                         | 0.054ms               | – 974.4 s   |  |
| Variable Gain Range (VGA)      | [dB]                    | -2.8 -                | - 41.5      |  |
| Refresh rate (MRR)             | [fps]                   | 28                    | 3.4         |  |
| Power consumption <sup>1</sup> |                         |                       |             |  |
| Stand by                       | [W]                     | 0.76                  |             |  |
| Maximum                        | [W]                     | 4.1                   |             |  |
| Dimensions/Mass                |                         |                       |             |  |
| height                         | [mm]                    | 60                    |             |  |
| width                          | [mm]                    | 60                    |             |  |
| depth                          | [mm]                    | 37.2                  |             |  |
| mass                           | [g]                     | 3                     | 20          |  |

table 3-8, MD061xU-SY, sensor and camera parameters

Notes: 1) Measured at 4TAP/52MHz readout



| Binning(readout) | TAPs | MD061CU-SY       | MD061MU-SY | Pixels      | Bit/px | fps  |
|------------------|------|------------------|------------|-------------|--------|------|
| 1x1              | 1    | Color            | B/W        | 2752 x 2204 | 14     | 7.7  |
| 2x2              | 1    | B/W <sup>1</sup> | B/W        | 1372 x 1100 | 14     | 14.3 |
| 3x3              | 1    | B/W <sup>1</sup> | B/W        | 912 x 732   | 14     | 20.0 |
| 4x4              | 1    | B/W <sup>1</sup> | B/W        | 684 x 548   | 14     | 25.0 |
| 5x5              | 1    | B/W <sup>1</sup> | B/W        | 548 x 438   | 14     | 29.4 |
| 1x1              | 2    | Color            | B/W        | 2752 x 2204 | 14     | 14.3 |
| 2x2              | 2    | B/W <sup>1</sup> | B/W        | 1372 x 1100 | 14     | 25.0 |
| 3x3              | 2    | B/W <sup>1</sup> | B/W        | 912 x 732   | 14     | 33.4 |
| 4x4              | 2    | B/W <sup>1</sup> | B/W        | 684 x 548   | 14     | 40.1 |
| 5x5              | 2    | B/W <sup>1</sup> | B/W        | 548 x 438   | 14     | 45.8 |
| 1x1              | 4    | Color            | B/W        | 2752 x 2204 | 14     | 28.4 |
| 2x2              | 4    | B/W¹             | B/W        | 1372 x 1100 | 14     | 49.6 |
| 3x3              | 4    | B/W <sup>1</sup> | B/W        | 912 x 732   | 14     | 66.0 |
| 4x4              | 4    | B/W¹             | B/W        | 684 x 548   | 14     | 78.9 |
| 5x5              | 4    | B/W <sup>1</sup> | B/W        | 548 x 438   | 14     | 90.4 |

table 3-9, MD061xU-SY, standard readout modes

Notes: 1) Image data contains some part of color information depending on binning

# 3.6.2.2. Quantum efficiency curves [%]

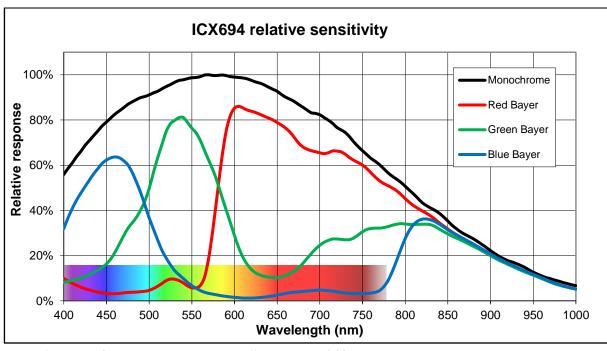


figure 3-8, ICX694-mono, color; quantum efficiency curve, @SONY

# 3.6.2.3. Dimensional drawings MD061xU-SY

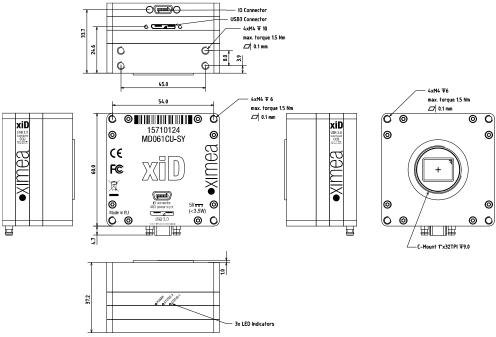


figure 3-9, dimensional drawing MD061xU-SY

### 3.6.2.4. Referenced documents

ICX694 Datasheets, Sony Corp.

### 3.6.2.5. Sensor features

| feature    | Note   |  |  |  |
|------------|--|--|--|--|
| Binning    | Yes, up to 5x5                                     |  |  |  |
| Skipping   | No   |  |  |  |
| ROI        | Vertical cropping results in increased read speed. |  |  |  |
| HW Trigger | Frigger without overlap                            |  |  |  |
| HDR        | lot available                                      |  |  |  |

table 3-10, sensor features available

# 3.6.3. MD091xU-SY

# 3.6.3.1. Sensor and camera parameters

| xiD model                      |                         | MD091CU-SY MD091MU-SY |             |  |
|--------------------------------|-------------------------|-----------------------|-------------|--|
| Sensor parameter               |                         |                       |             |  |
| Part number                    |                         | ICX814AQG             | ICX814ALG   |  |
| Color filter                   |                         | RGB Bayer mosaic      | None        |  |
| Туре                           |                         | CCD progr             | essive scan |  |
| Pixel Resolution (W x H)       | [pixel]                 | 3384                  | x 2708      |  |
| Active area size (W x H)       | [mm]                    | 12.5 x <sup>-</sup>   | 10.0mm      |  |
| Sensor diagonal                | [mm]                    | 16                    | mm          |  |
| Optical format                 | [inch]                  | 1                     | n           |  |
| Pixel Size                     | [µm]                    | 3.6                   | 9µm         |  |
| ADC resolution                 | [bit]                   | 1                     | 4           |  |
| FWC                            | [ke-]                   | 1                     | 2           |  |
| Dynamic range                  | [dB]                    | 64                    | 4.5         |  |
| SNR Max                        | [dB]                    | 43                    | 3.0         |  |
| Conversion gain                | [e-/LSB <sub>14</sub> ] | 0.926                 |             |  |
| Dark noise                     | [e-]                    | 9                     | .4          |  |
| Dark current                   | [e-/s]                  | TE                    | BD          |  |
| DSNU                           | [e-]                    | TBD                   |             |  |
| PRNU                           | %                       | TBD                   |             |  |
| Linearity                      | [%]                     | <1                    |             |  |
| Micro lenses                   |                         | Yes                   |             |  |
| Camera parameters              |                         |                       |             |  |
| Digitization                   | [bit]                   | 14                    |             |  |
| Supported bit resolutions      | [bit/pixel]             | 8, 10,                | 12, 14      |  |
| Exposure time (EXP)            |                         | 0.061ms               | – 1177 s    |  |
| Variable Gain Range (VGA)      | [dB]                    | -6.7 -                | - 37.6      |  |
| Refresh rate (MRR)             | [fps]                   | 19                    | 9.5         |  |
| Power consumption <sup>1</sup> |                         |                       |             |  |
| Stand by                       | [W]                     | 0.76                  |             |  |
| Maximum                        | [W]                     | 4.6                   |             |  |
| Dimensions/Mass                |                         |                       |             |  |
| height                         | [mm]                    | 60                    |             |  |
| width                          | [mm]                    | 60                    |             |  |
| depth                          | [mm]                    | 37.2                  |             |  |
| mass                           | [g]                     | 33                    | 20          |  |

table 3-11, MD091xU-SY, sensor and camera parameters

Notes: 1) Measured at 4TAP/52MHz readout



| Binning(readout) | TAPs | MD091CU-SY       | MD091MU-SY | Pixels      | Bit/px | fps  |
|------------------|------|------------------|------------|-------------|--------|------|
| 1x1              | 1    | Color            | B/W        | 3384 x 2708 | 14     | 5.2  |
| 2x2              | 1    | B/W¹             | B/W        | 1690 x 1352 | 14     | 9.8  |
| 3x3              | 1    | B/W¹             | B/W        | 1124 x 900  | 14     | 13.8 |
| 4x4              | 1    | B/W <sup>1</sup> | B/W        | 842 x 674   | 14     | 17.5 |
| 5x5              | 1    | B/W¹             | B/W        | 672 x 538   | 14     | 20.8 |
| 1x1              | 2    | Color            | B/W        | 3384 x 2708 | 14     | 9.8  |
| 2x2              | 2    | B/W <sup>1</sup> | B/W        | 1690 x 1352 | 14     | 17.5 |
| 3x3              | 2    | B/W¹             | B/W        | 1124 x 900  | 14     | 23.7 |
| 4x4              | 2    | B/W¹             | B/W        | 842 x 674   | 14     | 28.9 |
| 5x5              | 2    | B/W <sup>1</sup> | B/W        | 672 x 538   | 14     | 33.2 |
| 1x1              | 4    | Color            | B/W        | 3384 x 2708 | 14     | 19.5 |
| 2x2              | 4    | B/W¹             | B/W        | 1690 x 1352 | 14     | 34.8 |
| 3x3              | 4    | B/W <sup>1</sup> | B/W        | 1124 x 900  | 14     | 47.0 |
| 4x4              | 4    | B/W¹             | B/W        | 842 x 674   | 14     | 57.3 |
| 5x5              | 4    | B/W¹             | B/W        | 672 x 538   | 14     | 65.6 |

table 3-12, MD091xU-SY, standard readout modes

Notes: 1) Image data contains some part of color information depending on binning

# 3.6.3.2. Quantum efficiency curves [%]

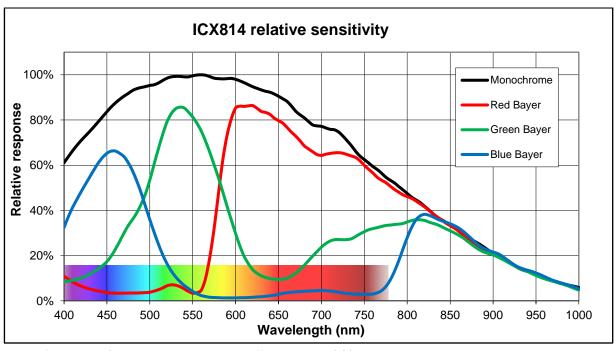


figure 3-10, ICX814-mono, color; quantum efficiency curve, @SONY

# 3.6.3.3. Dimensional drawings MD091xU-SY

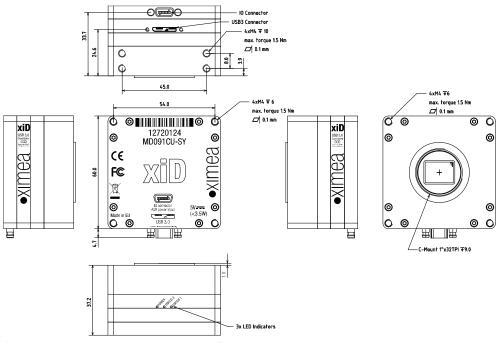


figure 3-11, dimensional drawing MD091xU-SY

### 3.6.3.4. Referenced documents

ICX814 Datasheets, Sony Corp.

### 3.6.3.5. Sensor features

| feature    | Note   |  |  |
|------------|--|--|--|
| Binning    | Yes, up to 5x5                                     |  |  |
| Skipping   | No   |  |  |
| ROI        | Vertical cropping results in increased read speed. |  |  |
| HW Trigger | Trigger without overlap                            |  |  |
| HDR        | Not available                                      |  |  |

table 3-13, sensor features available

# 3.6.4. MD120xU-SY

# 3.6.4.1. Sensor and camera parameters

| xiD model                      |                         | MD120CU-SY MD120MU-SY |               |  |
|--------------------------------|-------------------------|-----------------------|---------------|--|
| Sensor parameter               |                         |                       |               |  |
| Part number                    |                         | ICX834AQG             | ICX834ALG     |  |
| Color filter                   |                         | RGB Bayer mosaic      | None          |  |
| Туре                           |                         | CCD pro               | gressive scan |  |
| Pixel Resolution (W x H)       | [pixel]                 | 424                   | 4 x 2832      |  |
| Active area size (W x H)       | [mm]                    | 13.16                 | x 8.78mm      |  |
| Sensor diagonal                | [mm]                    | 15                    | 5.8 mm        |  |
| Optical format                 | [inch]                  |                       | 1"            |  |
| Pixel Size                     | [µm]                    | 3                     | 3.1µm         |  |
| ADC resolution                 | [bit]                   |                       | 14            |  |
| FWC                            | [ke-]                   |                       | 10            |  |
| Dynamic range                  | [dB]                    |                       | 61.7          |  |
| SNR Max                        | [dB]                    |                       | 41.1          |  |
| Conversion gain                | [e-/LSB <sub>14</sub> ] | 0.704                 |               |  |
| Dark noise                     | [e-]                    |                       | 9.3           |  |
| Dark current                   | [e-/s]                  | TBD                   |               |  |
| DSNU                           | [e-]                    | TBD                   |               |  |
| PRNU                           | %                       | TBD                   |               |  |
| Linearity                      | [%]                     | <1                    |               |  |
| Micro lenses                   |                         | Yes                   |               |  |
| Camera parameters              |                         |                       |               |  |
| Digitization                   | [bit]                   | 14                    |               |  |
| Supported bit resolutions      | [bit/pixel]             | 8, 10, 12, 14         |               |  |
| Exposure time (EXP)            |                         | 0.069n                | ns – 1450 s   |  |
| Variable Gain Range (VGA)      | [dB]                    | -8.8-                 | 3 – 35.5      |  |
| Refresh rate (MRR)             | [fps]                   | 15.8                  |               |  |
| Power consumption <sup>1</sup> |                         |                       |               |  |
| Stand by                       | [W]                     | 0.76                  |               |  |
| Maximum                        | [W]                     | 4.95                  |               |  |
| Dimensions/Mass                |                         |                       |               |  |
| height                         | [mm]                    | 60                    |               |  |
| width                          | [mm]                    | 60                    |               |  |
| depth                          | [mm]                    | 37.2                  |               |  |
| mass                           | [g]                     |                       | 320           |  |

table 3-14, MD120xU-SY, sensor and camera parameters

Notes: 1) Measured at 4TAP/52MHz readout



| Binning(readout) | TAPs | MD120CU-SY       | MD120MU-SY | Pixels      | Bit/px | fps  |
|------------------|------|------------------|------------|-------------|--------|------|
| 1x1              | 1    | Color            | B/W        | 4244 x 2832 | 14     | 4.0  |
| 2x2              | 1    | B/W <sup>1</sup> | B/W        | 2120 x 1414 | 14     | 7.7  |
| 3x3              | 1    | B/W <sup>1</sup> | B/W        | 1412 x 942  | 14     | 10.9 |
| 4x4              | 1    | B/W <sup>1</sup> | B/W        | 1056 x 704  | 14     | 14.0 |
| 5x5              | 1    | B/W <sup>1</sup> | B/W        | 844 x 562   | 14     | 16.7 |
| 1x1              | 2    | Color            | B/W        | 4244 x 2832 | 14     | 7.6  |
| 2x2              | 2    | B/W <sup>1</sup> | B/W        | 2120 x 1414 | 14     | 14.0 |
| 3x3              | 2    | B/W <sup>1</sup> | B/W        | 1412 x 942  | 14     | 19.2 |
| 4x4              | 2    | B/W <sup>1</sup> | B/W        | 1056 x 704  | 14     | 23.8 |
| 5x5              | 2    | B/W <sup>1</sup> | B/W        | 844 x 562   | 14     | 27.7 |
| 1x1              | 4    | Color            | B/W        | 4244 x 2832 | 14     | 15.2 |
| 2x2              | 4    | B/W <sup>1</sup> | B/W        | 2120 x 1414 | 14     | 27.7 |
| 3x3              | 4    | B/W <sup>1</sup> | B/W        | 1412 x 942  | 14     | 38.1 |
| 4x4              | 4    | B/W <sup>1</sup> | B/W        | 1056 x 704  | 14     | 47.1 |
| 5x5              | 4    | B/W <sup>1</sup> | B/W        | 844 x 562   | 14     | 54.6 |

table 3-15, MD120xU-SY, standard readout modes

Notes: 1) Image data contains some part of color information depending on binning

# 3.6.4.2. Quantum efficiency curves [%]

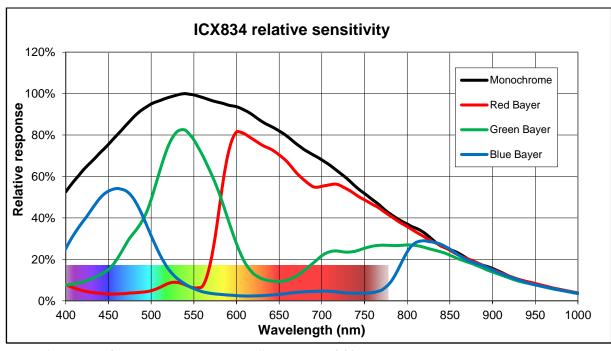


figure 3-12, ICX834-mono, color; quantum efficiency curve, @SONY

# 3.6.4.3. Dimensional drawings MD028xU-SY

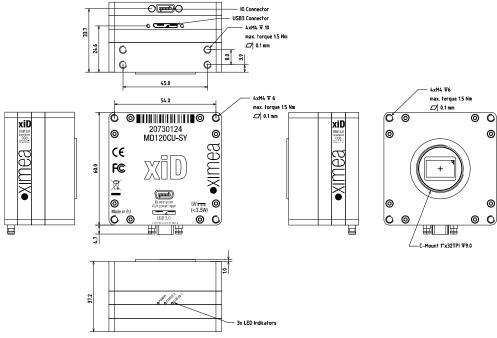


figure 3-13, dimensional drawing MD120xU-SY

### 3.6.4.4. Referenced documents

ICX834 Datasheets, Sony Corp.

### 3.6.4.5. Sensor features

| feature    | Note   |  |  |
|------------|--|--|--|
| Binning    | Yes, up to 5x5                                     |  |  |
| Skipping   | No   |  |  |
| ROI        | Vertical cropping results in increased read speed. |  |  |
| HW Trigger | Trigger without overlap                            |  |  |
| HDR        | Not available                                      |  |  |

table 3-16, sensor features available

# 3.7. User interface — LEDs

Three status LEDs are located on the back of the cameras, please see below.

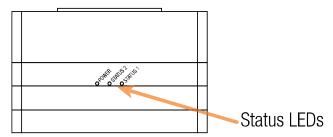


figure 3-14, position status LEDs

The LEDs are programmable. Please note the following description:

| LED     | Color  | Defaults          | Note              |
|---------|--------|-------------------|-------------------|
| POWER   | Orange | On                | User configurable |
| STATUS2 | Green  | Connection status | User configurable |
| STATUS1 | Red    | Streaming         | User configurable |

table 3-17, LED output description after camera is opened in API

The LEDs Status1 and Status2 are programmable. Please note the following description:

| LED      | Color        | Description  |
|----------|--------------|--|
| Power    | Orange       | Power indication: LED is on if the power is on (USB 3.0 cable connected) |
|          |              | User configurable:   |
|          |              | register (set value)   |
| Status 2 | Green        | USB 3.0 Enumeration  |
|          |              | USB 2.0 Enumeration (default),   |
|          |              | User configurable:   |
|          |              | register (set value)   |
|          |              | strobe   |
|          |              | busy   |
|          |              | streaming  |
|          |              | trigger  |
|          |              | level  |
|          |              | edge   |
|          |              | digital input  |
|          |              | slow blink   |
|          |              | fast blink   |
| Status 1 | Red          | Streaming (default),   |
|          |              | User configurable:   |
|          |              | register (set value)   |
|          |              | strobe   |
|          |              | busy   |
|          |              | streaming  |
|          |              | trigger  |
|          |              | level  |
|          |              | edge   |
|          |              | digital output   |
|          |              | slow blink   |
|          | 0 LED output | fast blink   |

table 3-18, LED output description



### 3.8. xiD USB 3.0 Interface

| Connector | Signals                                   | Mating Connectors                                   |
|-----------|---|---|
| USB 3.0   | Standard USB 3.0 Micro-B Female Connector | Standard USB 3.0 Micro-B Connector with thumbscrews |
|           |   | Screw thread M2, thread distance 18.0mm             |

table 3-19, USB 3.0 mating connector description

The USB 3.0 Micro-B connector is used for data transmission, camera control and power.

### 3.8.1. Location

The USB 3.0 connector is located on the back side of the camera:

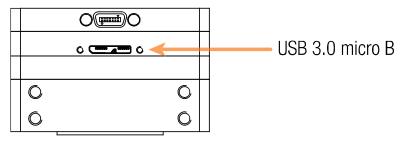


figure 3-15, position USB 3.0 interface

# 3.8.2. Pinning

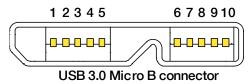


figure 3-16, pinning USB 3.0 connector

USB 3.0 Micro B connector (powered) Pin Assignment

| Pin | Signal     | Description                                 |
|-----|------------|---|
| 1   | VBUS       | Power                                       |
| 2   | D-         | LICE 2.0 signal pair                        |
| 3   | D+         | USB 2.0 signal pair                         |
| 4   | ID         | OTG Identification                          |
| 5   | GND        | Power Ground                                |
| 6   | MicB_SSTX- | LICE 2.0 CuperChood transmitter signal pair |
| 7   | MicB_SSTX+ | USB 3.0 SuperSpeed transmitter signal pair  |
| 8   | GND_DRAIN  | USB 3.0 signal Ground                       |
| 9   | MicB_SSRX- | LICE 2.0 CuparChand requirer signal pair    |
| 10  | MicB_SSRX+ | USB 3.0 SuperSpeed receiver signal pair     |

table 3-20, USB 3.0 connector, pin assignment

The USB 3.0 standard is backward compatible with the USB 2.0 interface.



# 3.9. Digital Input / Output (GPIO) Interface

xiD cameras use the 12-pin connector for the GPIO interface and power. MD features two pairs of IO ports. First one optocoupled rated for 24V systems and second insulated fast TTL. IO connector is located close to USB3.0 connector at the bottom of camera, featuring HDR-EA14LFYPG1 SLG connector.

| Connector  | Signals  | Mating Connectors          |
|------------|--|----------------------------|
| xiD        | Opto-isolated trigger input and insulated fast TTL | HDR-E14 MAG1+              |
| I/O 14-pin | 1/0  | HDR-E14-MSG1+              |
|            |  | IO pigtail cable Ximea PN: |
|            |  | CBL-MD-PWR-SYNC-3M0        |

table 3-21, GPIO mating connector description

### 3.9.1. Location

10 interface receptacle is located on the underside of the camera close to USB 3.0 connector:

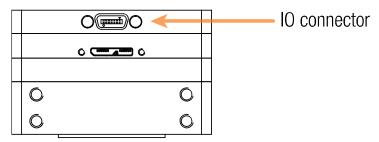


figure 3-17, position GPIO + power connector

### 3.9.2. IO Connector Pinning

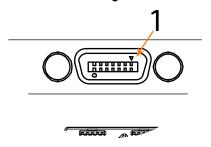


figure 3-18, xiD IO connector pinout



# I/O connector Pin Assignment:

| Pin     | Name   | Signal                                    | Technical description                   |
|---------|--------|---|---|
| 1       | GP01X  | Digital Output 1 pole X                   | 24V logic output (relay switch)         |
| 2       | GP01Y  | Digital Output 1 pole Y                   | 24V logic output (relay switch)         |
| 3       | GPI1+  | Digital Input 1 positive pole             | (<0.8 Low; 4-24 High)                   |
| 4       | GPI1-  | Digital Input 1 negative pole             | (<0.8 Low; 4-24 High)                   |
| 5       | GND 2  | Common Negative Pole 2 (GPI2, GPO2)       |   |
| 6       | GP02   | Magnetically isolated fast output         | Isolated output (<0.4 Low; IO2AUX High) |
| 7       | IO2AUX | Isolator Aux Power GPIO2 (3.3V - 5.0V DC) | Isolated output up to IO2AUX voltage    |
| 14      | GPI2   | Magnetically isolated fast input          | Isolated input (<0.8 Low; 2-5V High)    |
| 8,10,12 | NC     | Reserved for future use                   |   |
| 9,11,13 | NC     | Reserved for future use                   |   |
| Shell   |        | Shielding                                 |   |

table 3-22, xiD I/O connector Pin Assignment

# 3.9.3. Optically isolated Digital Input

### 3.9.3.1. Optically isolated Digital Input - General info

| Item  | Parameter / note   |
|---|--|
| Maximal input voltage                                       | 24V  |
| Common pole   | No   |
| Effect of incorrect input terminal connection               | Reverse voltage polarity protected                                 |
| Effects when withdrawing/inserting input module under power | No damage, no lost data  |
| Maximum recommended cable length                            | 5m   |
| Input level for logical 0                                   | Voltage < 2.0V/Current 0mA to 0.3mA                                |
| Input level for logical 1                                   | Voltage > 4.0V/Current 4mA to 6mA                                  |
| Input debounce filter                                       | No   |
| Input delay – rising edge <sup>1</sup>                      | <2 +/-0.5µs (Vinput=10V, Tambient=25°C)                            |
| Input delay – falling edge <sup>1</sup>                     | <40 +/-0.5μs (V <sub>INPUT</sub> =10V, T <sub>AMBIENT</sub> =25°C) |
| Number of inputs  | 1  |
| External trigger mapping                                    | Yes  |
| Input functions   | Trigger, get current level (rising or falling edge are supported)  |

table 3-23, Optically isolated digital input, general info

Note: -1) Propagation delay depends on voltage level and can vary from camera to camera, propagation jitter is significantly lower.

### 3.9.3.2. Digital Input – signal levels

Input levels are not IEC 61131-2, Type 1 as the ON state has been extended to support 5V TTL

| V-in-min [V] | V-in-max [V] | State     | I-max [mA]                 |
|--------------|--------------|-----------|----------------------------|
| -24.0        | 2.0          | Off (0)   | 0.0 – 0.3 mA (0mA nominal) |
| 2.0          | 4.0          | Transient | 4                          |
| 4.0          | 24.0         | On (1)    | 4 – 6 mA (5mA nominal)     |

table 3-24, digital info, signal levels

### Note:

- Input level **Vin** represents amplitude of the input signal.
- Voltage levels referenced to common ground GND

35

# 3.9.3.3. Digital Input – Internal Schematic

The internal scheme of Digital Input signal flow inside the camera is below.

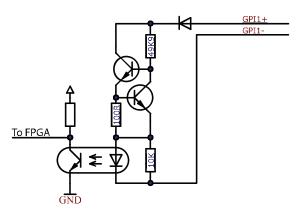


figure 3-19, digital input, interface schematic

# 3.9.3.4. Digital Input – Wiring

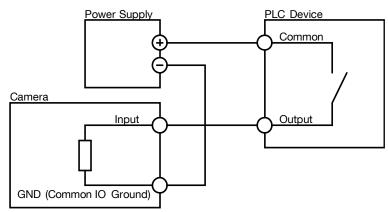


figure 3-20, digital input, interface wiring

### 3.9.3.5. Digital Input – Timing

Typical measured input delay between Digital Input to FPGA Input Measurements of input delays:

| Edge Type | Input Voltage [V] | Typ. delay [µs] |
|-----------|-------------------|-----------------|
| Rising    | 5                 | 1.8             |
| Rising    | 10                | 1.9             |
| Falling   | 5                 | 37              |
| Falling   | 10                | 39              |
| Falling   | 24                | 42              |

table 3-25, digital input, timing

Note: Measured at: Ambient Temperature 25°C

# 3.9.4. Optically isolated Digital Output

### 3.9.4.1. Optically isolated Digital Output - General info

| Item   | Parameter / note   |
|--|--|
| Maximal open circuit voltage                   | 24V  |
| Output port type                               | relay  |
| Protection                                     | short-circuit / over-current                                   |
| Protection circuit                             | PTC Resettable Fuse  |
| Maximal sink current                           | 100mA  |
| Trip current                                   | 290mA – self restarting when failure mode current disconnected |
| Inductive loads                                | No   |
| Effect of incorrect output terminal connection | The outputs are equivalent                                     |
| Maximal output drop                            | 0.5V, sink current 25mA  |
| Number of outputs                              | 1  |
| Strobe output mapping                          | Yes  |

table 3-26, Optically isolated digital output, general info

### 3.9.4.2. Optically isolated Digital Output Delay

| Output current | OFF -> ON | ON -> OFF | Note   |
|----------------|-----------|-----------|--|
| 2mA            | 29 μs     | 160 µs    | $V_{\text{source}} = 24V; R_{\text{LOAD}} = 12k0hm$          |
| 5mA            | 28 µs     | 165 µs    | $V_{\text{source}} = 24V; R_{\text{LOAD}} = 4.8 \text{kOhm}$ |
| 10mA           | 26.5 µs   | 172 μs    | $V_{\text{source}} = 24V; R_{\text{LOAD}} = 2.4\text{kOhm}$  |
| 24mA           | 26 µs     | 178 µs    | $V_{\text{source}} = 24V; R_{\text{LOAD}} = 1\text{kOhm}$    |

table 3-27, Optically isolated digital output, general info

Note: The delay can vary from device to device.

### 3.9.4.3. Optically isolated Digital Output – Internal schematic

Following scheme is the internal scheme of the Digital Output signal flow inside the camera.

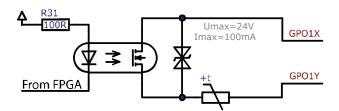


figure 3-21, digital output, interface schematic

# 3.9.4.4. Digital Output — Wiring

Digital output operates as relay. In most cases a power source for external device must be provided.

## 3.9.4.4.1. Connecting Digital OUTPUT to a NPN-compatible PLC device input (biased)

| Output state | Output switch state | Input state         |
|--------------|---------------------|---------------------|
| ON           | Sourcing current    | Pull up (energized) |
| OFF          | Relaxing            | Not energized       |

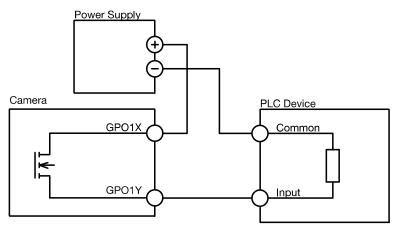


figure 3-22, Connecting Digital OUTPUT to a NPN-compatible PLC device input (biased)

#### Important note:

• If using this configuration, take into account that Common Ground connection may be biased by power supply for Digital Input!

## 3.9.4.4.2. Connecting Digital OUTPUT to a NPN-compatible PLC device input

This type of connection is possible only when opto-isolated input is used (bidirectional in some cases) or when only one general opto-isolated input is used.

| Output state | Output switch state | Input state           |  |
|--------------|---------------------|-----------------------|--|
| ON           | Sourcing current    | Pull down (energized) |  |
| OFF          | Relaxing            | Not energized         |  |

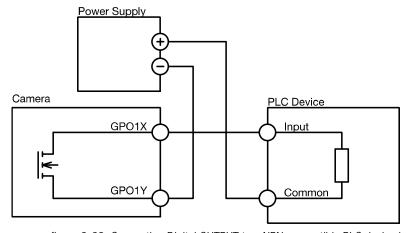


figure 3-23, Connecting Digital OUTPUT to a NPN-compatible PLC device input - more bidirectional inputs used

#### Note:

In this case a bidirectional opto-isolated input must be used



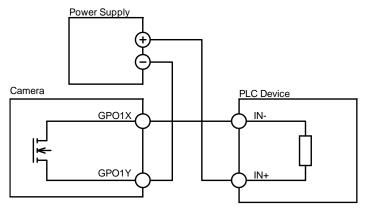


figure 3-24, Connecting Digital OUTPUT to a NPN-compatible PLC device - single input

# 3.9.4.4.3. Connecting Digital OUTPUT to a PNP-compatible device

| Output state Output switch state |     | Output switch state | Input state         |  |
|----------------------------------|-----|---------------------|---------------------|--|
|                                  | ON  | Sinking current     | Not energized       |  |
|                                  | OFF | Relaxing            | Pull up (energized) |  |

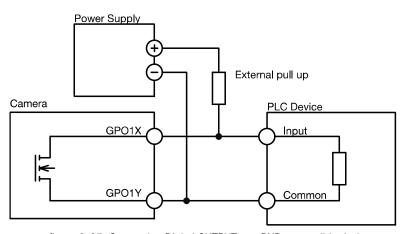


figure 3-25, Connecting Digital OUTPUT to a PNP-compatible device

Pull up resistor can be calculated as follows:  $R = \frac{V_{psu} - V_{input}}{I_{input}}$ 

Where:

 $\textit{V}_{\textit{psu}}$   $\;\;$  power supply voltage. Must be higher than required input amplitude

 $V_{input}$  required input amplitude

 $I_{input}$  input driving current (corresponding to input amplitude)

Remember to use the appropriate resistor power rating  $P(R) > (V_{psu} - V_{input}) * I_{input}$ 

# 3.9.4.4.4. Output Wiring Example: LED Driving

LED can be driven directly by camera digital output. A series resistor must be used to limit LED current.

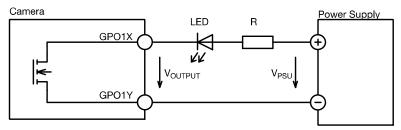


figure 3-26, LED Driving

LED series resistor can be calculated by the following equation:  $R = \frac{V_{psu} - V_{output} - V_{led}}{I_{led}}$ 

#### Where:

 $V_{psu}$  power supply voltage (5V to 24V)

 $V_{output}$  voltage across digital output pins (see. <u>3.9.4.1 Optically isolated Digital Output - General info</u>)

 $V_{led}$  LED forward voltage (see table below)

I<sub>led</sub> LED current

#### Note:

• Remember to use the appropriate resistor power rating  $P(RES) = I_{led}^2 \times R = (V_{psu} - V_{led}) \times I_{led}$ 

### Typical LED forward voltage

| LED Colour       | V <sub>led</sub> (typ.) | V <sub>led</sub> (max.) | Note         |
|------------------|-------------------------|-------------------------|--------------|
| Standard Red     | 1.7V                    | 2.1V                    |              |
| Super Bright Red | 1.85V                   | 2.5V                    |              |
| Low power Red    | 1.7V                    | 2.0V                    |              |
| Orange           | 2.0V                    | 2.1V                    |              |
| Yellow           | 2.1V                    | 2.2V                    |              |
| Green            | 1.9V                    | 2.5V                    |              |
| Emerald Green    | 2.1V                    | 2.7V                    |              |
| Blue             | 2.5V                    | 3.7V                    |              |
| White            | 2.8V                    | 3.8V                    | _            |
| Infra-Red        | 1.3V                    | 1.8V                    | Opto coupler |

table 3-28, digital output, LED driving



# 3.9.4.4.5. Output Wiring Example: Inductive load (Relay) Driving

Do not connect inductive load RL directly to Camera Digital Output. A transistor must be used to prevent damage of the output. See image below for possible inductive load driving. Resistor R can be connected to Digital Outputs and power supply to provide the necessary bias current for transistor. You should also use an external diode to protect the transistor from over voltage while disconnecting an inductive load. Keep in mind that this connection has an inverted logic. Current will flow through the load at the start of the camera.

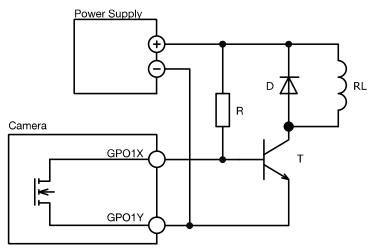


figure 3-27, Inductive load (Relay) Driving (inverted logic)

For positive logic you can use a second bipolar transistor.

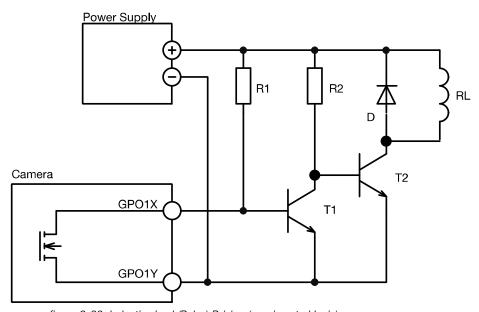


figure 3-28, Inductive load (Relay) Driving (non-inverted logic)



# 3.9.5. Isolated High Speed Digital Lines (GPI2, GPO2)

xiD camera features one input and one output with fast digital isolation

Non isolated Digital lines can be used as inputs or outputs compatible with TTL logic. These are high impedance pins so when used as output high impedance slave input has to be used.

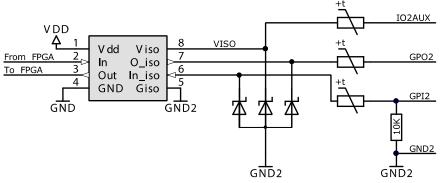


figure 3-29, Isolated low voltage Digital Lines, interface schematic

# 3.9.5.1. Isolated High Speed Digital Lines General info

| Item  | Parameter / note  |
|---|---|
| Maximal input voltage IO2AUX                                | 5.5V  |
| Common pole   | Yes   |
| Number of inputs  | 1   |
| Number of outputs   | 1   |
| Protection circuit  | PTC Resettable Fuse   |
| Effect of incorrect input terminal connection               | Reverse voltage polarity protected                                |
| Effects when withdrawing/inserting input module under power | No damage, no lost data   |
| Maximum recommended cable length                            | 5m  |
| Input level for logical 0                                   | Voltage < 0.8V  |
| Input level for logical 1                                   | Voltage > 2.6V  |
| Input debounce filter                                       | No  |
| Input delay – rising edge <sup>1</sup>                      | <70ns (Vinput=3.3V, Tambient=25°C)                                |
| Input delay – falling edge <sup>1</sup>                     | <70ns (Vinput=3.3V, Tambient=25°C)                                |
| External trigger mapping                                    | Yes   |
| Input functions   | Trigger, get current level (rising or falling edge are supported) |
| Output maximal sink current                                 | 10mA  |
| Trip current  | 21mA – self restarting when failure mode current disconnected     |
| Inductive loads   | No  |
| Output level for logical 0                                  | Voltage < 0.3V (IO2AUX = 3.3V) < 0.5 (IO2AUX = 5V)                |
| Ouptut level for logical 1                                  | Voltage > 2.5V/1mA (IO2AUX = 3.3V) >3.9V/1mA (IO2AUX = 5V)        |
| Number of outputs   | 1   |
| Strobe output mapping                                       | Yes   |

Table 3-29, General info for Isolated low voltage Digital Lines.

# 3.10. CBL-U3-1M0 / CBL-U3-3M0 / CBL-U3-5M0

#### 1.0m / 3.0m / 5.0m USB 3.0 cables

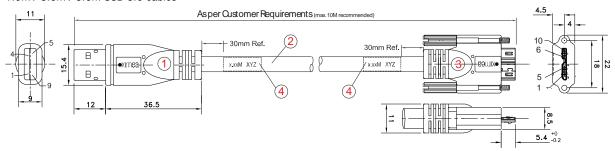


figure 3-30, drawing USB3 cable

| Item | Description   |
|------|---|
| 1    | USB A 3.0 9 pin Molded Plug <blk></blk>                             |
| 2    | MCD-USB-211 [OD= 7.3mm] <blk></blk>                                 |
| 3    | 3 USB MicB 3.0 sl 10 pin Molded Plug with Screw Locking <blk></blk> |
| 4    | Cable Label   |

table 3-30, USB3 cable, components

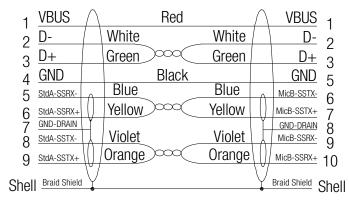


figure 3-31, wiring USB3 cable

| Pin | Signal     | Description                                 |  |
|-----|------------|---|--|
| 1   | VBUS       | Power                                       |  |
| 2   | D-         | LICE 2.0 signal pair                        |  |
| 3   | D+         | USB 2.0 signal pair                         |  |
| 4   | ID         | OTG Identification                          |  |
| 5   | GND        | Power Ground                                |  |
| 6   | MicB_SSTX- | LICE 2 0 CuparChand transmitter signal pair |  |
| 7   | MicB_SSTX+ | USB 3.0 SuperSpeed transmitter signal pair  |  |
| 8   | GND_DRAIN  | USB 3.0 signal Ground                       |  |
| 9   | MicB_SSRX- | LICE 2.0 CuparChand receiver signal pair    |  |
| 10  | MicB_SSRX+ | USB 3.0 SuperSpeed receiver signal pair     |  |

table 3-31, USB3 connector, pin assignment

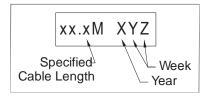


figure 3-32, label details USB3 cable

# 3.11. CBL-U3-3M0-ANG

3.0m USB 3.0 cable, angled micro USB3 connector

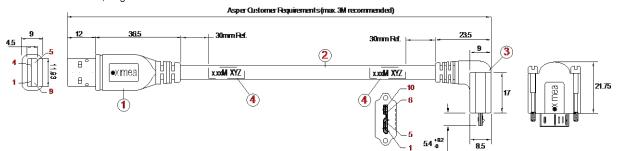


figure 3-33, drawing USB3 cable angled

| Item | Description   |
|------|---|
| 1    | USB A 3.0 9 pin Molded Plug <blk></blk>                           |
| 2    | A12-7143 [OD=5.9mm] <blk> UL20726 2STP#30 + 1UTP#28 + 2C#26</blk> |
| 3    | USB MicB 3.0 sl 90D A1(10 pin Molded Plug) <blk></blk>            |
| 4    | Cable Label   |

table 3-32, USB3 cable angled, components

| 1      | VBUS                    | $\triangle$ | Red               |        | $\triangle$  | VBUS 1                      |
|--------|-------------------------|-------------|-------------------|--------|--------------|-----------------------------|
| 2      | <u>D-</u>               |             | White             | White  | <u> </u>     | D- 2                        |
| 3      | D+                      |             | Green             | Green  |              | D+ 3                        |
| 4      | GND                     |             | Black             |        |              | GND 5                       |
| 5      | StdA-SSRX-              | Λ           | Blue              | Blue   | Λ            | MicB-SSTX- 6                |
| 6      | StdA-SSRX+              |             | Yellow >          | Yellow |              | MicB-SSTX+ 7                |
| 7<br>8 | GND-DRAIN<br>StdA-SSTX- | Ť           | Violet            | Violet | <del> </del> | GND-DRAIN 8<br>MicB-SSRX- 9 |
| 9      | StdA-SSTX+              | W/          | Orange ) $\infty$ | Orange | \ <i>\</i>   | MicB-SSRX+ 10               |
| Shell  | Braid Shield            | Y           |                   |        | Y            | Braid Shield Shell          |

figure 3-34, wiring USB3 cable angled

| Pin | Signal     | Description                                 |
|-----|------------|---|
| 1   | VBUS       | Power                                       |
| 2   | D-         | LICE Of cignal pair                         |
| 3   | D+         | USB 2.0 signal pair                         |
| 4   | ID         | OTG Identification                          |
| 5   | GND        | Power Ground                                |
| 6   | MicB_SSTX- | LICE 2.0 CuparChand transmitter signal pair |
| 7   | MicB_SSTX+ | USB 3.0 SuperSpeed transmitter signal pair  |
| 8   | GND_DRAIN  | USB 3.0 signal Ground                       |
| 9   | MicB_SSRX- | LICE 2.0 CurarCraad receiver signal rain    |
| 10  | MicB_SSRX+ | USB 3.0 SuperSpeed receiver signal pair     |

table 3-33, USB3 connector, pin assignment

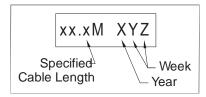
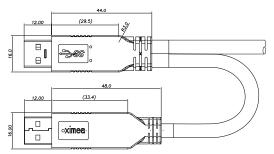


figure 3-35, label details USB3 cable angled

# 3.12. CBL-U3-3M0-YP / CBL-U3-5M0-YP

3.0m / 5.0m USB 3.0 Y-cables providing additional power from secondary USB port.



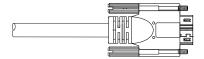


figure 3-36, drawing USB3 cable

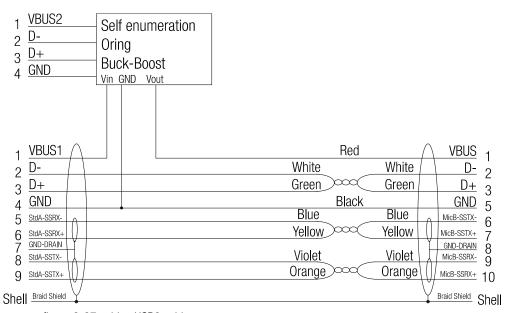


figure 3-37, wiring USB3 cable

| Pin | Signal     | Description                                |  |
|-----|------------|--|--|
| 1   | VBUS       | Power                                      |  |
| 2   | D-         | LICD 2.0 signal pair                       |  |
| 3   | D+         | USB 2.0 signal pair                        |  |
| 4   | ID         | OTG Identification                         |  |
| 5   | GND        | Power Ground                               |  |
| 6   | MicB_SSTX- | USB 3.0 SuperSpeed transmitter signal pair |  |
| 7   | MicB_SSTX+ | USB 3.0 Superspeed transmitter signal pail |  |
| 8   | GND_DRAIN  | USB 3.0 signal Ground                      |  |
| 9   | MicB_SSRX- | USB 3.0 SuperSpeed receiver signal pair    |  |
| 10  | MicB_SSRX+ |  |  |

table 3-34, USB3 connector, pin assignment

# 3.13. CBL-U3-3M0-YP-UA / CBL-U3-5M0-YP-UA

3.0m USB 3.0 Y-cables providing additional power from secondary USB port. Featuring angled micro USB3 connector.

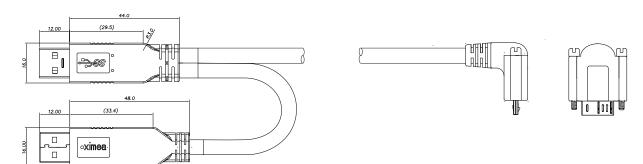


figure 3-38, drawing USB3 cable angled

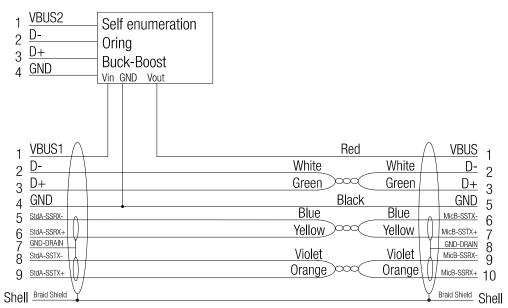


figure 3-39, wiring USB3 cable angled

| Pin | Signal     | Description                                 |  |
|-----|------------|---|--|
| 1   | VBUS       | Power                                       |  |
| 2   | D-         | LICD 2.0 signal pair                        |  |
| 3   | D+         | USB 2.0 signal pair                         |  |
| 4   | ID         | OTG Identification                          |  |
| 5   | GND        | Power Ground                                |  |
| 6   | MicB_SSTX- | LICD 2 0 CuparChand transmitter signal pair |  |
| 7   | MicB_SSTX+ | USB 3.0 SuperSpeed transmitter signal pair  |  |
| 8   | GND_DRAIN  | USB 3.0 signal Ground                       |  |
| 9   | MicB_SSRX- | LICE 2.0 CuparCpand receiver signal pair    |  |
| 10  | MicB_SSRX+ | USB 3.0 SuperSpeed receiver signal pair     |  |

table 3-35, USB3 connector, pin assignment

# 3.14. USB 3 host adapters

USB 3.0 to PCI Express x1 Gen2 Host Card



figure 3-40, USB3 host adapters

Please refer to following page <a href="https://www.ximea.com/support/projects/usb3/wiki/USB\_3\_Host\_Adapters">https://www.ximea.com/support/projects/usb3/wiki/USB\_3\_Host\_Adapters</a> for more information.

### System requirements

All requirements depend on selected host adapter. Please refer to host adapter specification

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# 3.15. CBL-MD-PWR-SYNC-3M0

The following is a description of the sync cable for the xiD camera line.

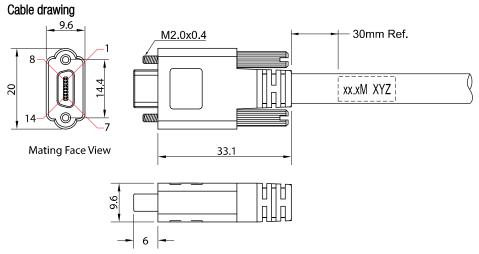


figure 3-41, drawing sync cable - current revisions of this cable are 3m in length

| Pin       | color  | Signal   |  |  |
|-----------|--------|--|--|--|
| 1         | Blue   | GP01X - Opto-isolated Output                       |  |  |
| 2         | Green  | GPO1Y - Opto-isolated Output                       |  |  |
| 3         | Violet | GPI1+ - Opto-isolated Input                        |  |  |
| 4         | Yellow | GPI1 Opto-isolated Input                           |  |  |
| 5         | Brown  | GND 2 Common Negative Pole 2 (GPI2, GPO2)          |  |  |
| 6         | White  | GPO2 - Magnetically isolated fast output           |  |  |
| 7         | Grey   | IO2AUX - Isolator Aux Power GPIO2 (3.3V - 5.0V DC) |  |  |
| 14        | Orange | GPI2 - Magnetically isolated fast input            |  |  |
| 8, 10, 12 | Black  | AUX power input negative pole (5.0V)               |  |  |
| 9, 11, 13 | Red    | AUX power input positive pole (5.0V)               |  |  |

table 3-36, IO/AUX cable, pin assignment

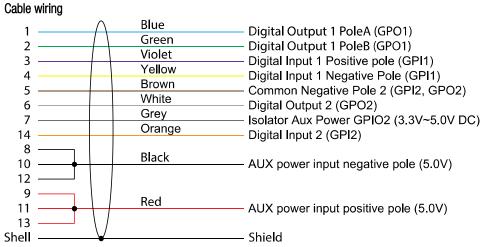


figure 3-42, wiring sync cable CBL-MD-PWR-SYNC-3M0

# 3.16. Tripod Adapter – MECH-60MM-BRACKET-T

xiD series tripod mounting bracket

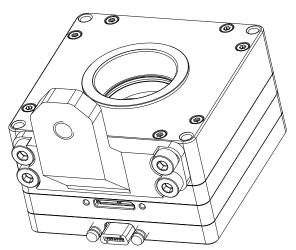


figure 3-43, mounting tripod adapter

Use 4x M4 screws provided with bracket as a kit for mounting. Bracket can be mounted on the bottom of camera.

# 3.16.1. Dimensional drawings

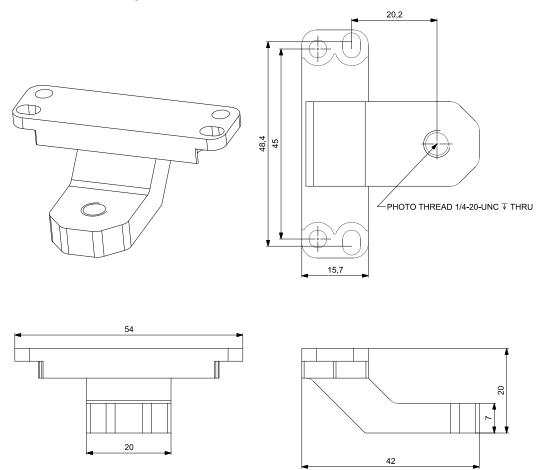


figure 3-44, dimensional drawing tripod adapter

# 4. Operation

For a proper operation of your xiD camera there are certain requirements that have to be met. You will read more about these requirements in the following chapters, as well as a description of how to use a xiD camera. Please check our website for the most up to date information.

# 4.1. System Requirements

# 4.1.1. Software Requirements

The xid cameras are compatible with the following operating systems:

- Windows 10
- Windows 7 SP1
- Linux Ubuntu
- MacOS 10.8 or newer



All XIMEA cameras are compatible with the most advanced Vision and Image Processing Libraries.

See chapter <u>5 Software</u> for more information about the options to access a xiD cameras, as well as a list of currently supported libraries and frameworks supported in Windows.

For more information visit page: https://www.ximea.com/support/wiki/apis/APIs

### 4.1.2. Hardware Requirements

The XIMEA xiD cameras are compatible with USB 3.1, USB 3.0. Please note, that the highest performance can only be achieved by using high performance USB 3.1 or USB 3.0 ports.

Please note details and the most recent info at:

Recommended hardware http://www.ximea.com/support/wiki/usb3/Compatible hardware

# 4.1.2.1. System Configuration

#### Minimum system configuration:

For a basic operation of your xiD camera with a PC the following minimum system configuration is required. Please note that bandwidth and processing performance are tied to the hardware configuration and the minimum hardware configuration could lead to a reduced bandwidth and limited frame rate.

CPU: Intel i3 or better RAM: 2GB RAM or more

Disc Space: 200 MB of free disc space

Video: NVIDIA or Radeon graphics card 128MB or integrated on CPU

Ports: Motherboard with USB 2.0 or USB 3.0 port or PCle x1-16 Gen 2 slot for compatible USB 3.0 host adapter

See 3.14 USB 3 host adapters

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#### Recommended system configuration:

For best processing performance and bandwidth we recommend to use the following system configuration. This is essential when using the higher resolution models for achieving maximum frame rate.

CPU: Intel i7

RAM: 8GB RAM or more

Disc Space: 400 MB of free disc space

Video: NVIDIA or Radeon graphics card 128MB

Ports: Motherboard with USB 2.0 or USB 3.0 port or PCle x1-16 Gen 2 slot for compatible USB 3.0 host adapter

See 3.14 USB 3 host adapters

### 4.1.2.2. USB 3.1 Host Adapter

For a stable operation of your xiC camera and achieving the maximum possible system performance with the highest frame rate it is important to choose an appropriate USB 3.1 host adapter chipset.

Please have a look at the following link to our webpage: <a href="http://www.ximea.com/support/wiki/usb3/Compatible\_hardware">http://www.ximea.com/support/wiki/usb3/Compatible\_hardware</a> XIMEA maintains a regularly updated overview of compatible USB 3.0 and USB 3.1 host adapters together with the available bandwidth <a href="https://www.ximea.com/support/projects/usb3/wiki/USB\_3\_Host\_Adapters">https://www.ximea.com/support/projects/usb3/wiki/USB\_3\_Host\_Adapters</a>

The maximum data transfer rate depends on different conditions (motherboard, chipset, driver version, operating system,...).

**PCI Express (PCIe) bus speed requirement**: To achieve maximum performance of USB3 cameras - USB 3.1 host adapter must be connected to the PCIe slot/port/hub and running at 5GT/s in case of PCIe Gen2 host adapters. For cards requiring Gen3 the speed needs to be 8GT/s.

#### 4.1.2.3. Cables

The USB 3.1 Gen1 cable that you use with the xiD camera is responsible for the power supply and the data transfer to the PC. It is required to use an industrial USB 3.1 Gen1 cable with a proper wiring and shielding. We recommend using XIMEA industrial USB 3.1 Gen1 cables in order to achieve the maximum possible performance of the camera.

XIMEA offers several passive USB 3.1 Gen1 cables and a sync cables, please see paragraphs 3.10, 3.11, 3.12 and 3.13

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### 4.2. Video Formats

### 4.2.1. Full Resolution

By default, each camera outputs a full resolution image based on its sensor specification.

# 4.2.2. ROIs – Region Of Interest

ROI, also called area-of-interest (AOI) or windowing, allows the user to specify a sub-area of the original sensor size for read-out. Depending on the sensor inside xiD cameras support the definition of one single ROI by specifying the size (width and height) as well as the position (based on upper left corner) of the of the sub-area.

Please note 3.6 Model Specific Characteristics

# 4.2.3. Downsampling Modes

Downsampling describes the possibility of reducing the image resolution without affecting the sensors physical size, ie. without reducing the physical size of the sensing area. This feature is useful when optics are used, that are particularly fitted to a certain sensor size and if it is necessary to maintain the full image circle on the sensor.

Downsampling can be achieved in two ways: binning and skipping.

# 4.2.3.1. Binning

When binning is applied, the image is divided into cluster of  $k^*l$  pixels, where all pixels in each cluster are interpolated and result in the value of one output pixel. For example, a  $2^*2$  binning produces  $2^*2$  pixel clusters and results in images with  $\frac{1}{4}$  of the original resolution.

### **4.2.3.2.** Skipping

When skipping is chosen, only every n-th pixel is used to create the output image. For example, with a 2x1 vertical skipping, every odd number line used and every even number line is skipped, resulting in an image with half its original vertical resolution. Skipping is a faster downsampling mode, but also introduces more aliasing effects.

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# 4.2.4. Image Data Output Formats

All modes are provided by the xiAPI or standard interfaces using the xiAPI (please note <u>5.1 Accessing the Camera</u>). Each xiD camera supports several Image Data Output Formats.

| Mode               | Description   |  |  |  |
|--------------------|---|--|--|--|
| RAW8               | Raw sensor data, 8 Bit per pixel, single channel  |  |  |  |
| RAW16              | Raw sensor data, 16 Bit per pixel, single channel   |  |  |  |
|                    | 10,12 or 14 Bit sensor output (LSB) with bit-shift up to 16 Bit   |  |  |  |
| MON08              | Intensity output, 8 Bit per pixel, single channel   |  |  |  |
| MONO16             | Intensity output, 16 Bit per pixel, single channel  |  |  |  |
| RGB24              | RGB filtered output, 24 Bit per pixel, 3 channels Sequence: [Blue][Green][Red]  |  |  |  |
| RGB32              | RGBA filtered output, 32 Bit per pixel, 4 channels, Alpha channel equals 0. Sequence: [Blue][Green][Red][0]   |  |  |  |
| RGB_PLANAR         | RGB filtered output with planar-oriented channels. Format: [R][R][G][G][B][B]   |  |  |  |
| FRM_TRANSPORT_DATA | Data from transport layer (e.g. packed). This format is optimal when an efficient storage and later (offline) processing is required.  Format is defined by XI_PRM_TRANSPORT_PIXEL_FORMAT |  |  |  |

table 4-1, image formats,

Note1: For color modes **RGB32** and **RGB24** the image from sensor needs to be pre-processed (de-bayering). CPU load is higher in these modes. Setting this parameter will reset current region of interest. **RGB24** is being processed from the **RGB32** by removing the unused Alpha channel creating a slightly higher CPU load than the **RGB32** format.

Note2: The color filtering (de-bayering) relies on the interpolation of adjacent pixels in order to create pixel in the target image. Pixels on the edges of the image are missing adjacent pixels and therefore cannot be used for the interpolation process. The result is a target image that is smaller than the source image (4 pixels on all sides).

Note3: For most formats the transport data can be packed. 12-bit pixel bit depth transfers only 12bit per pixel compared to 16bit per pixel when the data are not packed. In case of packed format the CPU load is higher due to unpacking of the image data. Available bandwidth is however used optimally.

# 4.3. Acquisition modes

### 4.3.1. Free-Run

Also known as continuous acquisition. In this mode the sensor delivers a constant stream of image data at the maximum speed available by the current bandwidth, without any external trigger. Each image exposure is sequentially started automatically when possible.

For all sensors the exposure of the next frame overlaps with the data readout of the previous frame.

This Overlapped mode gives the highest number of frames per second (FPS).

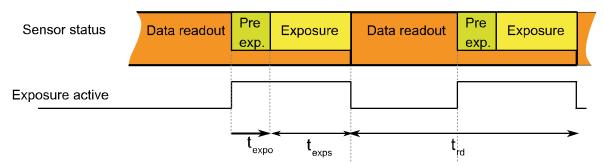


figure 4-1, acquisition mode - free run

In this mode the timing depends on the Exposure Time and Data Readout Time.

All xiD cameras support setting FPS. This however works in non-overlapped mode. When set the camera will internally generate trigger pulses with desired FPS. Please see: <a href="mailto:Frame\_Rate\_Control">Frame\_Rate\_Control</a>:

https://www.ximea.com/support/wiki/allprod/Frame\_Rate\_Control

This is also applicable in case of triggered acquisition.

## 4.3.2. Trigger controlled Acquisition/Exposure

Unlike in the free-run, each image exposure can also be triggered with an input trigger signal. In this mode, the sensor waits in stage until the trigger signal arrives. Only then, the exposure of first frame is started, which is followed by the data readout. XIMEA cameras supports several triggered modes along with single image exposure after one trigger. The trigger signal can be either edge sensitive or level sensitive. In the case of 'level sensitive', it can be used to control length of exposure or acquisition itself.

Generally trigger sources can be divided into two groups:

### Software Trigger

The trigger signal can be sent to the sensor using a software command. In this case, common system related latencies and jitter apply.

#### Hardware Trigger

A hardware trigger can be sent to the sensor using the digital input described in <u>3.9.3 Optically isolated Digital Input</u>, or non-isolated ports configured as input described in <u>3.9.5 Isolated High Speed Digital Lines</u>. Triggering by hardware is usually used to reduce latencies and jitter in applications that require the most accurate timing.

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## 4.3.2.1. Triggered mode without overlap - single frame

This mode gives lower FPS compared to Free-Run mode and lower FPS.

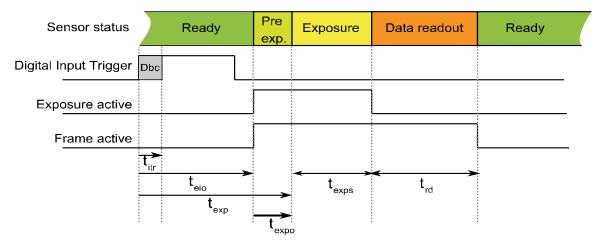


figure 4-2, acquisition mode - triggered without overlap

In this mode the timing depends on sum of:

- Input transition time (t<sub>itr</sub>), depends on:
  - o Digital Input Delay time for changing internal circuit to active state.
  - o Input Debouncing Time time for stabilizing uneven input signals (e.g. from mechanical switches). This time can be set using xiAPI with parameters XI\_PRM\_DEBOUNCE\_EN and XI\_PRM\_DEBOUNCE\_TO on some cameras. Default 0.
- Exposure time (see t<sub>exps</sub> above).
- Data Readout time (see t<sub>rd</sub> above)

#### Typical times for selected camera models

| Camera Model            | DownS | t <sub>itr</sub> [µs] | t <sub>exp</sub> [µs] | t <sub>eio</sub> [µs]          | t <sub>expo</sub> [µs] | t <sub>rd</sub> [µs] |
|-------------------------|-------|-----------------------|-----------------------|--------------------------------|------------------------|----------------------|
| MDnnnxU-SY <sup>1</sup> | any   | Х                     | 2/40/<1/<1            | See output delays <sup>2</sup> | 0                      | Note 3               |

table 4-2, trigger mode w/o overlap, timing

#### Notes:

- 1) nnnx in model name means all available models where n is number and x is M or C.
- 2)  $t_{exp}$  GPI1 rising edge/ GPI1 falling edge/ GPI2 rising edge/ GPI2 falling edge
- 3) t<sub>rd</sub> readout time depends on many parameters (badwidth, number of taps, readout frequency, ROI, ...)

#### **Description:**

DownS = Current camera DownSampling (XI\_PRM\_DOWNSAMPLING)

teio = Trigger (Digital Input) to Strobe (Digital Output) (on some models is listed: Off->On change / On->Off change)

t<sub>exo</sub> = Strobe (Sensor) to Digital Output (on some models is listed: Off->On change / On->Off change)

t<sub>expo</sub> = Start of exposition to Exposure Active Digital Output

 $t_{exps}$  = Current Exposure Time set (XI PRM EXPOSURE)

Conditions: XI\_PRM\_DEBOUNCE\_EN=0 (off).

#### Minimum trigger period (T<sub>trig\_min</sub>)

Minimum trigger period can be calculated using the following formula:

$$t_{trig\_min} = t_{exp} + t_{exps} + t_{rd}$$

The minimum trigger period can be calculated from XI\_PRM\_FRAMERATE parameter read from API after all acquisition parameters have been set.  $t_{trig\_min} = 1/fps$ 

# 4.3.2.2. Triggered acquisition - burst of frames

### Frame Burst Start

In this mode each trigger pulse triggers defined number of exposed frames. This mode is available only if the camera is set to frame rate mode.

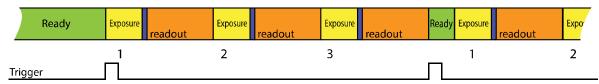


figure 4-3, triggered burst of frames – frame burst start, number of frames in burst set to 3 (maximal frame rate)

#### Frame Burst Active

If trigger is level sensitive it can be used to control image acquisition. This mode is available only if the camera is set to frame rate mode.

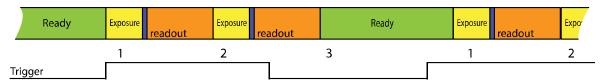


figure 4-4, triggered burst of frames – frame burst active (maximal frame rate)

Please see: Frame Burst Modes: https://www.ximea.com/support/wiki/allprod/Frame\_Burst\_Modes

### 4.4. Camera Parameters and Features

## 4.4.1. Exposure Time

Also known as shutter speed. This parameter defines the length of the integration period for each frame.

Most CMOS sensors generate the exposure interval internally. For some it is possible to control it by external signaling. The sensor internal timing depends on the provided system clock. Most sensors use dividers to generate slower clocks for internal usage.

The minimum exposure time is defined mostly by row times, where the row time ( $T_R$ ) is dependent on various internal settings. Very few sensors support exposure times equal to zero. There is a defined minimum exposure time as well as minimum steps between possible exposure times. There is also a maximum exposure time, defined by sensor architecture.

#### 4.4.2. Gain

The gain value influences the analog-to-digital conversion process of the image sensor pipeline and acts as a multiplier of the output signal. Using gain values greater than 0 will increase the pixel intensities but may also increase the overall noise level. For some camera models the gain can be set in discrete steps only.

# 4.5. Host-Assisted Image Processing Parameters Available in xiAPI.

## 4.5.1. Auto Exposure – Auto Gain

When AEAG is used, every captured image is evaluated for its mean intensity. Based on the result, the exposure and gain values are modified with the objective to achieve a target intensity level for the following image. Further, the maximum applicable exposure and gain values can be defined. Since both, exposure and gain, have an influence on the intensity, the ratio between those two parameters in their contribution to the algorithm can also be set (exposure priority).

#### 4.5.2. White Balance

Only for color models: The white balance can be adjusted with three coefficients kR, kG and kB, one for each color channel. These coefficients can be set individually in order to increase or decrease each channel's contribution and therefore allow the user to control the color tint of the image.

#### 4.5.2.1. Assisted Manual White Balance

This feature measures the white balance a single time and sets the white balance coefficient to achieve a mean grey (neutral) tint. The measurement is performed on the central rectangle of the image, with  $1/8^{th}$  of its width and height. The function expects a white sheet of paper exposed to 50% of the intensity values (8 Bit RGB values should be around 128) to be visible.

### 4.5.2.2. Auto White Balance

The white balance is measured across the full image for every  $4^{th}$  image that is acquired and the white balance coefficients are set to to achieve a neutral colour tint.

### 4.5.3. Gamma

Only for color models: As a part of the color filtering process, it is possible to adjust the gamma level of the image. The adjustment can be set separately for the luminosity and the chromaticity.

### 4.5.4. Sharpness

Only for color models: As a part of the color filtering process, it is possible to adjust the sharpness of the image.



### 4.5.5. Color Correction Matrix

The color correction matrix is a 4x4-matrix which is applied on each pixel of an image in a host-assisted port-processing step. For example, this Matrix can be used to adjust the brightness, contrast, and saturation.

### 4.5.6. Sensor Defect Correction

During the manufacturing process, every camera is tested for various type of defects and a list of the measured defect pixels is created and stored in the camera's non-volatile memory. This list is then used for the correction of acquired images during operation. The correction is inactive by default, but can be turned on by the user if a non-processed output is required.

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# Software

# 5.1. Accessing the Camera

Depending on the target application, the user can choose between several ways of accessing and controlling the camera. These can be divided into two categories: a programmatic approach, through programming code, or an integrated approach, through a supported, GUI based software package. The programmatic approach is generally used for the development of a custom application or image processing pipeline. The integrated approach is favored, if the specific toolset of a certain software package is sufficient and the camera serves as an integrated capture device.

# 5.1.1. Proprietary API

All XIMEA cameras are supported by the same unified APIs (application programming interface). The API is a software interface between the camera system driver and the application. Different APIs are available for different programming environments, e.g. xiAPI (see  $\underline{5.7.1 \ XIMEA \ APIs}$ ) for C/C++ developments and xiAPI.Net for C#/.Net based developments

### 5.1.2. Standard Interface

As an alternative to the proprietary API, the camera can be accessed through a set of standard interfaces. These interfaces decouple a specific hardware design (e.g. physical interface) of a camera from its control in software. Therefore multiple camera classes and types can be used in a unified way.

#### 5.1.2.1. GenlCam

**GenlCam/GenTL** provides a camera-agnostic transport layer interface to acquire images or other data and to communicate with a device. Each camera serves as a *GenTL Producer* which can be accessed in all software packages that are compatible with the GeniCam standard, as well as through custom developments which implement this standard interface.

# 5.1.3. Vision Library Integration

All XIMEA cameras are compatible with the most advanced vision and image processing libraries. For GUI based software packages, the cameras can be directly accessed without the need of programming. Code libraries are generally used in conjunction with one of our APIs, in order to add additional functionality (e.g. image processing, communication, data storage).

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# 5.2. XIMEA CamTool

The CamTool is a cross-platform application showcasing the features of all XIMEA camera families.



### Short description

It runs on Windows, Linux, macOS systems offering a substantial imaging tool set, which can be further extended with custom modules using a plugin infrastructure. CamTool is based on Qt for the UI and xiAPI for the camera control. Its camera settings menu resembles the parameter set of the xiAPI

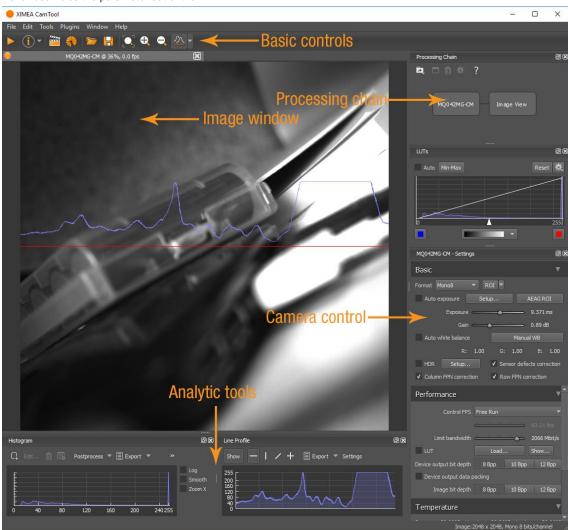


figure 5-1, CamTool Layout

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#### **Functions**

- to see live image from multiple XIMEA cameras connected
- control the camera parameters
- store of camera image and video
- analyze the image properties
- histogram and line profile
- image averaging, image flip/mirror
- software trigger timer, save/load camera and program settings
- LUT (Look up table)
- Lua scripting

CamTool allows to operate all connected cameras simultaneously. In this case all control are layered for the cameras. Basic controls are placed as tabs in upper part of the window. Image window can be detached from application if needed. Amount of visible camera controls depend on visibility level which can be set in Edit→Options.

CamTool allows to save static images in various formats like: \*.TIFF, \*.BMP, \*.JPEG, \*.PNG, \*.GIF, \*.DNG and \*. FITS as well as the image sequences in multipage tiff (\*.tif), avi file (\*.avi) and Image sequence (\*.xiseq). With additional plugin it is possible to save H.264 compressed video.

For more information please refer to: https://www.ximea.com/support/wiki/allprod/XIMEA\_CamTool

# **5.3.** Supported Vision Libraries

For an up-to-date listing of the supported vision libraries and software packages, visit our web site <a href="http://www.ximea.com/support/projects/vision-libraries/wiki">http://www.ximea.com/support/projects/vision-libraries/wiki</a>.

# 5.3.1. Libraries maintained by XIMEA

All cameras listed in the section Products are supported with these libraries.

XIMEA commits to update the API within twelve months after a new major release.

XIMEA warranties backwards compatibility of these software packages for two major releases.

#### 5.3.1.1. MathWorks MATLAB



MathWorks® is the leading developer and supplier of software for technical computing and Model-Based Design.

More: http://www.mathworks.de/ or https://www.ximea.com/support/wiki/vision-libraries/MathWorks Matlab

### 5.3.1.2. MVTec HALCON



HALCON is the comprehensive standard software for machine vision with an integrated development environment (IDE) that is used worldwide.

More: http://www.mvtec.com/halcon/ or https://www.ximea.com/support/wiki/vision-libraries/MVTec\_HALCON

#### 5.3.1.3. National Instruments LabVIEW Vision Library



**LabVIEW** is a graphical programming environment.

More: http://www.ni.com/labview/

https://www.ximea.com/support/wiki/vision-libraries/National Instruments LabVIEW

### 5.3.1.4. OpenCV



OpenCV is an open source library of programming functions mainly aimed at real time computer vision, developed by Intel and now supported by Willow Garage.

More: https://opencv.org/

https://www.ximea.com/support/wiki/vision-libraries/OpenCV

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# 5.4. XIMEA Windows Software Package

XIMEA API Software Package can be installed on: Microsoft Windows 10, Microsoft Windows 8, Microsoft Windows 7 (and Microsoft Windows 7 Embedded), Microsoft Windows Server 2008 R2.

#### 5.4.1. Contents

The package contains:

- OS Drivers of all XIMEA camera types for OS Microsoft Windows 7 SP1 32/64 bit, Windows 8 32/64 bit, Windows Server 2008 R2 x86-64, Windows 10 32/64 bit.
- APIs (xiAPI, xiAPI.NET, xiApiPtyhon)
- Examples
- CamTool
- xiCop
- *GenTL Producer* for connection of *GenTL Consumer* applications.
- Vision Libraries integration demonstrations:
  - NI LabView interface xiLib

### 5.4.2. Installation

- Download and execute the XIMEA API Software Package installer (EXE-file, approximate size 100 MB): http://www.ximea.com/downloads/recent/XIMEA Installer.exe
- Read the License Agreement.
- Start the installer

Be sure that you have administrator privileges or start the Installer with administrator rights (right click and select "run as administrator):

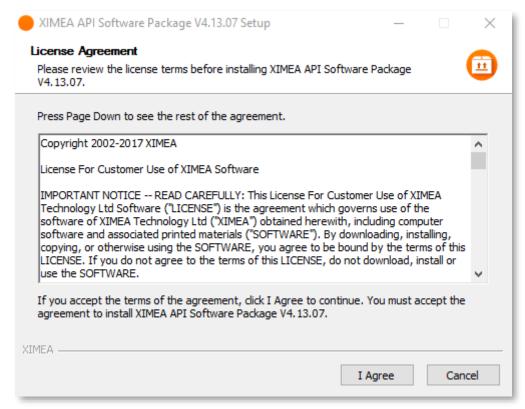


figure 5-2, XIMEA Windows Software Package installation - 1



 Select the Software components you want to install. You can uncheck the components you don't want to install, but it is recommended to leave them all checked.

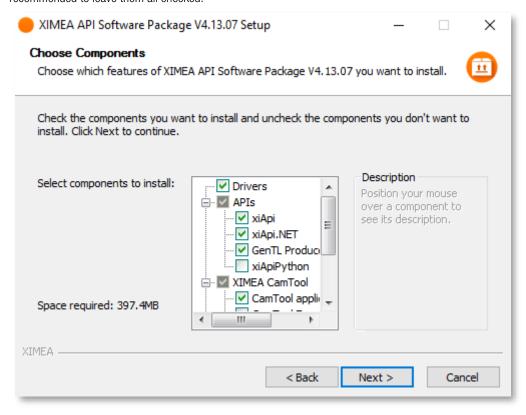


figure 5-3, XIMEA Windows Software Package installation - 2

• Specify the install location - you can leave the default location or change it to your desired location.

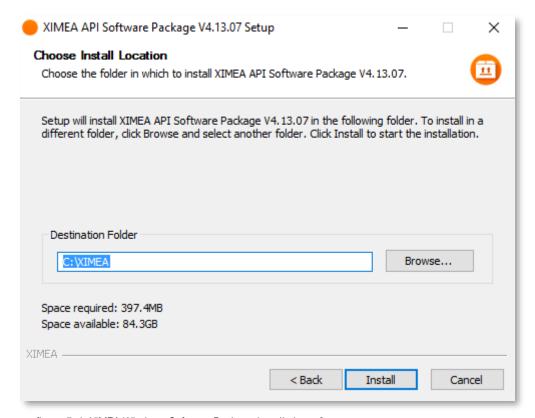


figure 5-4, XIMEA Windows Software Package installation - 3

 Now the XIMEA API Software Package should start copying files, updating System Variables and installing drivers if necessary.

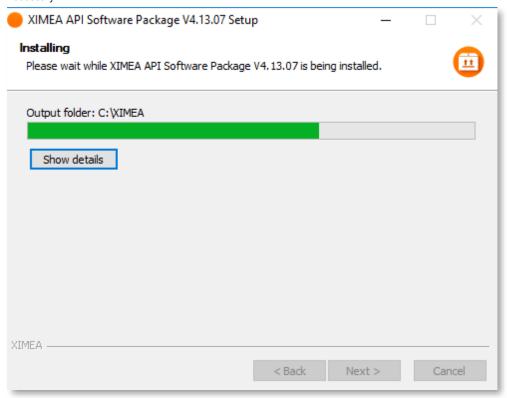


figure 5-5, xiAPI installation, Windows - 4

Installation is completed.

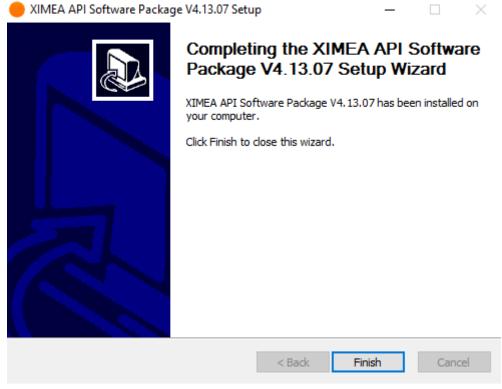


figure 5-6, xiAPI installation, Windows - 5

Finish.

# 5.5. XIMEA Linux Software Package

XIMEA Linux Software Package is tarred installer with files that can be run on Linux Ubuntu 14.04 and 16.04 (32 and 64 Bit) and newer releases.

# 5.5.1. Contents

The package contains:

- Driver (beta version) for XIMEA USB2 and USB3 cameras
- xiAPI
- Ximea CamTool
- Examples:
  - o xiSample sample showing basic image acquisition in xiAPI

### 5.5.2. Installation

Download XIMEA Linux Software Package
 wget http://www.ximea.com/downloads/recent/XIMEA Linux SP.tgz

figure 5-7, XIMEA Linux Software Package installation - 1

 Untar tar xzf XIMEA\_Linux\_SP.tgz cd package

Start installation script

./install

```
ximea@ximea-Linux64: ~/package
ximea@ximea-Linux64:~$ tar xzf XIMEA_Linux_SP.tgz
ximea@ximea-Linux64:~$ cd package
ximea@ximea-Linux64:~/package$ ./install -cam_usb30
This will install XIMEA Linux Package after 5 seconds
To abort installation - press Ctrl-C
Instaling x64 bit version
[sudo] password for ximea:
This is installation of package for platform -x64
Checking if user is super user
ΟK
WARNING!!!
You have enabled experimental USB3 support! It may affect USB2 support too.
DO NOT downgrade the kernel to versions older than 3.4!!!
Advised way of enabling USB3 support is upgrading kernel to version at least as new as 3.6.
If you decide to do it in the future, rerun this installation script after rebooting into new ke
rnel.
Installing libusb
Installing Firewire support - libraw1394
Checking Firewire stack
Installing API library
OK
OK
ок
Rebuilding linker cache
Installing XIMEA-GenTL library
Installing vaViewer
Installing streamViewer
Installing xiSample
Creating desktop link for vaViewer
Creating desktop link for streamViewer
Installing udev rules for USB and Firewire cameras
oĸ
Note:
You may need to reconnect your USB and/or Firewire cameras
Also check that you are in the "plugdev" group
More info:
http://www.ximea.com/support/wiki/apis/Linux_USB20_Support
For GeniCam - please add GENICAM_GENTL64_PATH=/opt/XIMEA/lib/libXIMEA_GenTL.so to Your .bashrc
o enable GenTL
Now applications can be started. E.g. /opt/XIMEA/bin/xiSample
ximea@ximea-Linux64:~/package$
```

figure 5-8, XIMEA Linux Software Package installation - 2

1) **Note:** If logged in user is not root, you will be asked for your password to get root access, because the installation runs with root account using *sudo*.

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# 5.6. XIMEA macOS Software Package

XIMEA macOS Software Package is native DMG installer that can be run on macOS 10.8 (Mountain Lion) or newer.

### 5.6.1. Contents

The package contains:

- Driver (beta version) for XIMEA USB2 and USB3 cameras
- xiAPI
- XIMEA CamTool
- Examples:
  - o xiSample sample showing basic image acquisition in xiAPI

### 5.6.2. Installation

Before installing XIMEA macOS Software Package it may be necessary to modify security settings on your computer. The new feature of OS X 10.8 called GateKeeper can prevent you from using our macOS Software Package due to the fact that the current version is unsigned.

Open System Preferences application and click on Security & Privacy.



figure 5-9, XIMEA macOS Software Package installation - 1



On the General Tab select the option Anywhere under Allow applications downloaded from:



figure 5-10, xiAPI installation, MacOS - 2

- Download XIMEA macOS Software. Package: http://www.ximea.com/downloads/recent/XIMEA\_OSX\_SP.dmg
- Mount it by double-clicking this file in Finder.
- Run the install script to install XiAPI on your macOS system
- A window with package contents will open.

### 5.6.3. Start XIMEA CamTool

- Connect camera
- Start Applications / XIMEA CamTool
- Start acquisition by clicking on orange triangle at upper left corner of CamTool

# 5.7. Programming

# 5.7.1. XIMEA APIs

- xiAPI Streamlined API. The standard API for C/C++ based projects, see <u>5.7.2 xiAPI Overview</u>.
- xiAPI.NET Managed .NET Common Language Runtime (CLR) API.
   xiAPI.NET is designed as a wrapper around xiAPI and therefore shares most of its functionality.
- xiApiPython Integrated API into PYTHON.

### 5.7.2. xiAPI Overview

xiAPI stands for XIMEA Application Programming Interface. It is a common interface for all XIMEA cameras.

#### Architecture

API is a software interface between the camera system driver and application.

- On Windows: xiAPI is compiled into xiapi32.dll or xiapi64.dll
- On Linux: xiAPI is compiled into /usr/lib/libm3api.so

#### Installation

xiAPI is part of all current XIMEA software packages for Windows, Linux and MacOS. For information on the software packages, see 5 Software

### 5.7.3. xiAPI Functions Description

The core of xiAPI consists of the following functions, which allow controlling of the camera functionality.

```
// get the number of discovered devices.
XI RETURN xiGetNumberDevices (OUT DWORD *pNumberDevices);
// open interface
XI RETURN xiOpenDevice(IN DWORD DevId, OUT PHANDLE hDevice);
// get parameter
XI RETURN xiGetParam(IN HANDLE hDevice, const char* prm, void* val,
DWORD * size, XI PRM TYPE * type);
// set parameter
XI RETURN xiSetParam(IN HANDLE hDevice, const char* prm, void* val,
DWORD size, XI_PRM_TYPE type);
// start the data acquisition
XI RETURN xiStartAcquisition(IN HANDLE hDevice);
// acquire image and return image information
XI RETURN xiGetImage(IN HANDLE hDevice, IN DWORD TimeOut, INOUT XI IMG
* img);
// stop the data acquisition
XI RETURN xiStopAcquisition(IN HANDLE hDevice);
// close interface
XI RETURN xiCloseDevice(IN HANDLE hDevice);
```

## 5.7.4. xiAPI Parameters Description

For a complete list of available parameter, please visit the xiAPI online manual at <a href="http://www.ximea.com/support/wiki/apis/XiAPI\_Manual">http://www.ximea.com/support/wiki/apis/XiAPI\_Manual</a>

**Note:** Since xiAPI is a unified programming interface for all of XIMEA's cameras, not all of the described parameters apply for every camera and sensor model.

All functions in xiAPI return status values in form of the XI\_RETURN structure which is defined in xiApi.h. If a parameter is not supported by a certain camera, the return value will represent a respective error code (e.g. 106 - Parameter not supported).

# 5.7.5. xiAPI Examples

### 5.7.5.1. Connect Device

This example shows the enumeration of available devices. If any device was found the first device (with index 0) is opened.

```
HANDLE xiH = NULL;

// Get number of camera devices
DWORD dwNumberOfDevices = 0;
xiGetNumberDevices(&dwNumberOfDevices);

if (!dwNumberOfDevices)
{
   printf("No camera found\n");
}
else
{
   // Retrieving a handle to the camera device xiOpenDevice(0, &xiH);
}
```

#### 5.7.5.2. Parameterize Device

This example shows how an exposure time is set. Next, the maximum possible downsampling rate is retrieved and the result is set as new downsampling rate.

```
// Setting "exposure" parameter (10ms)
int time_us = 10000;
xiSetParam(xiH, XI_PRM_EXPOSURE, &time_us, sizeof(time_us),
xiTypeInteger);

// Getting maxium possible downsampling rate
int dspl_max = 1;
xiGetParamInt(xiH, XI_PRM_DOWNSAMPLING XI_PRM_INFO_MAX, &dspl_max);

// Setting maxium possible downsampling rate
xiSetParamInt(xiH, XI_PRM_DOWNSAMPLING, dspl_max);
```

### 5.7.5.3. Acquire Images

This example shows how the acquisition is started on the device with the handle xiH, ten images are acquired in a row and the acquisition is stopped.

```
xiStartAcquisition(xiH);
#define EXPECTED_IMAGES 10
for (int images=0;images < EXPECTED_IMAGES;images++)
{
    // getting image from camera
    xiGetImage(xiH, 5000, &image);
    printf("Image %d (%dx%d) received from camera\n", images,
        (int)image.width, (int)image.height);
}
xiStopAcquisition(xiH);</pre>
```

### 5.7.5.4. Control Digital Input / Output (GPIO)

#### Hardware Trigger and Exposure Active output

In this setup each image is triggered by a Digital Input Trigger. After the image is triggered, it can be transferred using xiGetImage.

This setup ensures a low latency between the trigger signal and image Exposure start. This time should be less than 10µs.

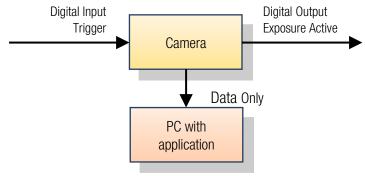


figure 5-11, GPIO - schematic

```
HANDLE xiH;
xiOpenDevice(0, & xiH);

// select trigger source
xiSetParamInt(xiH, XI_PRM_TRG_SOURCE, XI_TRG_EDGE_RISING);

// select input pin 1 mode
xiSetParamInt(xiH, XI_PRM_GPI_SELECTOR, 1);
xiSetParamInt(xiH, XI_PRM_GPI_MODE, XI_GPI_TRIGGER)

// set digital output 1 mode
xiSetParamInt(xiH, XI_PRM_GPO_SELECTOR, 1);
xiSetParamInt(xiH, XI_PRM_GPO_MODE, XI_GPO_EXPOSURE_ACTIVE);
xiSetParamInt(xiH, XI_PRM_GPO_MODE, XI_GPO_EXPOSURE_ACTIVE);
xiStartAcquisition(handle1);

// Trigger signal should start image exposure within timeout
#define TIMEOUT_IMAGE_WAITING_MS 10000
xiGetImage(handle, TIMEOUT_IMAGE_WAITING_MS, &image);
// process image here...
```

### 5.7.6. xiAPI Auto Bandwidth Calculation

xiAPI uses Auto Bandwidth Calculation (ABC) before the opening of each camera by default. After the measurement, 90% of the measured value is used as the maximum allowed transfer speed of the camera to ensure the stability of transfer.



It is important to set this parameter to XI\_OFF to ensure highest possible data transfer speed.

To disable ABC, the application should set parameter XI\_PRM\_AUTO\_BANDWIDTH\_CALCULATION to XI\_OFF before the first xiOpenDevice is used. This setting disabled ABC and the camera stream is not limited.

# 5.7.7. GenlCam

For more information on programing according the GenlCam standard, please visit the standard's website at <a href="http://www.emva.org/standards-technology/genicam/">http://www.emva.org/standards-technology/genicam/</a>

#### 5.8. XIMEA Control Panel

The XIMEA Control Panel (xiCOP), is a diagnostics and management tool for all XIMEA cameras. xiCOP is currently only available for Windows operating system.

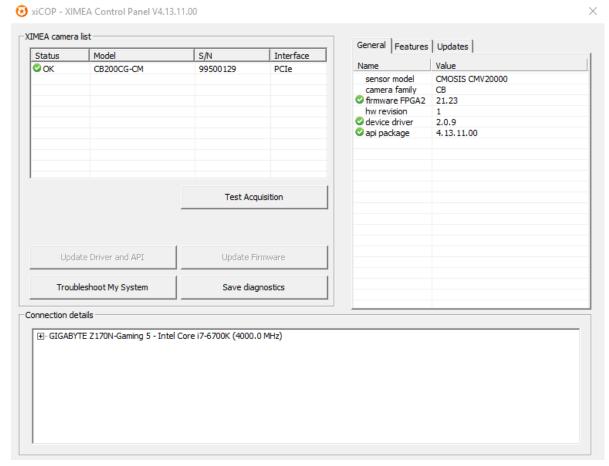


figure 5-12, xiCOP

#### **Features**

- Facilitates diagnostics of system performance bottlenecks. xiCOP is capable of retrieving the system's hardware tree, thus problematic hardware configurations can be identified.
- Diagnosis of firmware and software compatibility.
   xiCOP checks relevant firmware and software versions and warns if a component is not up-to-date.
- List all currently attached XIMEA devices and their features.
- Suggests solution for diagnosed issues.
- One click update to the latest XIMEA API Software Package.
- One click update of firmware in selected cameras.

# 6. Appendix

# 6.1. Troubleshooting and Support

This chapter explains how to proceed, if you have issues in getting your xiD camera to a proper operation. At first, please make sure, that you have installed the latest version of the following XIMEA software:

XIMEA Windows Software Package http://www.ximea.com/downloads/recent/XIMEA\_Installer.exe

Please make sure, that you have connected your xiD camera with the XIMEA USB 3.0 cable to an appropriate USB 3.0 port. Ensure that the connections are carefully locked. Follow the instructions described in chapter <u>5.2 XIMEA CamTool</u> (run the xiD camera with the Ximea CamTool). In case that you still have issues, please read the following chapters. If this does not at first work, please check all your connections to the camera and then try the latest 'beta' version of API with the most recent fixes: <a href="https://www.ximea.com/downloads/recent">https://www.ximea.com/downloads/recent</a> beta/XIMEA Installer.exe

In case that you still have issues, please read the following chapters.

# 6.1.1. Worldwide Support

We offer worldwide first level support to you by our partners.

Please refer to your local dealer if you need technical support for your xiD camera.

# **6.1.2.** Before Contacting Technical Support

There are a few steps to take before contacting your local dealer for technical support. In case you cannot display images from your xiD camera, please open the XIMEA xiCOP software (please see <u>5.8 XIMEA Control Panel</u>). It will immediately start searching for connected cameras. Your camera will appear in the XIMEA camera list on the upper left side of the xiCOP window if it is connected properly and your USB interface meets the minimum system requirements described in <u>4.1 System Requirements</u>. If the camera does not appear, please proceed with the following steps:

| Step no: | Description   |
|----------|---|
| 1        | Click on the button "Troubleshoot My System" and follow the instructions that are suggested.  |
| 2        | If step 1 does not lead to a positive result, please click the button "Save diagnostics". Keep the diagnostic file ready for providing it to support.   |
| 3        | Contact your local dealer where you bought the camera either by phone or by email for first level support. He will decide if he can help you immediately or if more information is necessary for initiating the next steps. |

table 6-1, use xiCOP before contacting technical support

# 6.1.3. Frequently Asked Questions

In this manual, we can list only a few FAQ. For more and updated information, please also note:

- Frequently Asked Questions http://www.ximea.com/support/wiki/allprod/Frequently Asked Questions
- Knowledge Base http://www.ximea.com/support/wiki/allprod/Knowledge\_Base



#### 6.1.3.1. What is the real transfer speed?

xiD camera can deliver up to 400 Mbyte/sec. This requires that certain conditions are met, see <u>4.1 System Requirements</u>. Maximum transfer speeds of different interfaces:

| Interface       | Transfer speed | Usable bandwidth | System costs |
|-----------------|----------------|------------------|--------------|
| IEEE1394A       | 400 Mbit/s     | 45 MByte/sec     | Medium       |
| CameraLink base | 2.04 Gbit/s    | 255 MByte/sec    | High         |
| GigE            | 1024 Mbit/s    | 100 MByte/sec    | Medium       |
| USB 2.0         | 480 Mbit/s     | 49 MByte/sec     | Low          |
| USB 3.0         | 5 Gbit/s       | 450 MByte/sec    | Low          |
| USB 3.1 (gen1)  | 5 Gbit/s       | 450 MByte/sec    | Low          |
| PCle gen2 x2    | 8 Gbit/s       |                  | Low          |
| PCle gen2 x4    | 16Gbit/s       | _                | Low          |
| PCle gen3 x8    | 64Gbit/s       | _                | Low          |

table 6-2, interface depending transfer rates

#### 6.1.3.2. Why can I not achieve maximum transfer speed?

In order to reliably achieve maximum transfer speed it is necessary to verify that you are using recommended hardware (please see 4.1 System Requirements), and that all software requirements are met.

xiCOP (please see <u>5.8 XIMEA Control Panel</u>) - XIMEA Control Panel free software tool, facilitates the task of verification of XIMEA camera installations.

# 6.2. Product service request (PSR)

If you experienced any unexpected behavior of your xiD camera, please follow the steps described below:

## 6.2.1. Step 1 - Contact Support

If your xiD camera is not working as expected, please contact your local dealer for troubleshooting the product and determine the eligibility of a Product Service Request (PSR).

In case you were asked to create a PSR by your local contact, please continue to STEP 2

**NOTE**: Your product must be UNDER WARRANTY in order to qualify for a free repair or replacement.

#### 6.2.2. Step 2 - Create Product Service Request (PSR)

- Read the XIMEA General Terms & Conditions http://www.ximea.com/en/corporate/generaltc
- Open the XIMEA Product Service Request form http://www.ximea.com/support/projects/service/issues/new
- Fill in all fields
- Confirm with the button "Create"

#### 6.2.3. Step 3 - Wait for PSR Approval

Our support personnel will verify the PSR for validity.

If your PSR is valid, it will be approved for sending the camera to us. This is done usually within 24 business hours. After that you will get a PSR Approval email (sent to the email address that you have entered in the field "Contact person — email").

The email contains:

- shipping instructions
- attached document containing the Product Service Request Number (PSRN)

When you received the PSR Approval email - please continue to Step 4.

In case your PSR was rejected – please do not send your camera to XIMEA.

## 6.2.4. Step 4 - Sending the camera to XIMEA

If possible, send the camera back in the original package. If not possible, please pack the camera in a way that it cannot be damaged during shipment and send it back as described in the PSR Approval email that you have received.

## 6.2.5. Step 5 - Waiting for Service Conclusion

Once we have received the camera, we will send you a notification. The XIMEA Service will then check the status of the camera that you have sent for a possible repair. Depending on warranty conditions, product status and agreement one of the following operations will be performed:

| Operation   | Repair costs paid by | Return delivery costs paid by |
|---|----------------------|-------------------------------|
| repaired in warranty                                | XIMEA                | XIMEA                         |
| replaced in warranty                                | XIMEA                | XIMEA                         |
| repaired for cost                                   | Customer             | Customer                      |
| not repaired and returned                           | -                    | Customer                      |
| not repaired and discarded if requested by customer | -                    | -                             |

table 6-3, service operations overview

If the camera will be returned, you will receive the tracking number. In this case, please continue to step 6

## 6.2.6. STEP 6 - Waiting for return delivery

After you have received the return shipment, please confirm it by changing the status of the PSR to "Received by customer".

# 6.3. Safety instructions and precautions

This chapter describes safety instructions and precautions valid for xiD cameras and special considerations regarding XIMEA board level cameras. In order to avoid harm or damage your xiD camera, please handle it like described in this manual, paying special attention to the cautions shown in the following table:

# 6.3.1. Disassembling

Do not disassemble the camera except for conversion to CS-Mount, see <u>3.3 Lens Mount</u>.

There are no switches or parts inside the cameras that requires any kind of mechanical adjustment. Please note that the warranty is voided by opening the camera housing.

#### 6.3.2. Mounting / Screwing

Use only the designated threaded holes for mounting the camera. Please note the camera / bracket drawings in chapter  $\underline{3.6}$   $\underline{Model Specific Characteristics}$  and  $\underline{3.16 Tripod Adapter} - \underline{MECH-60MM-BRACKET-T}$ 

Use only the specified screws and torques when fastening, see 3.4 Mounting points.

# 6.3.3. Connections

Use only recommended connectors and cables. Please check the system requirements described in 4.1 System Requirements.

#### 6.3.4. Power supply

See 3.1 Power Supply

The I/O connection is not usable for powering the camera.

#### 6.3.5. Environment / protect against water

Use camera in acceptable environment only, please note the descriptions in 3.2.1 Environment

Protect the camera against contact with water. Do not let camera get wet.

Damages may be caused by:

- Overheating
- Contact with water
- Operation in an environment with condensing humidity
- Mechanical shock

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# 6.3.6. Recommended light conditions.

Do not expose the camera to light sources with intense energy, e.g. laser beams or X-ray.

Light intensity or exposure time exceeding the saturation of the sensor may damage the sensor irreparably. This may occur e.g. in the following situations:

- · High-energy laser light hitting the sensor directly
- Bright light sources hitting the sensor directly (burn-in)
- Camera is exposed to X-rays

The warranty does not cover damaged cameras caused by X-ray applications or too much light / laser light.



## 6.3.7. Protect the optical components

Do not touch the optical components with hard or abrasive objects.

When handling the camera, avoid touching the lenses and filter glasses. Fingerprints or other impurities may affect the image quality and may damage the surfaces.

Mount / dismount lenses and additional filters only in a dust free environment.

Do not use compressed air as this could push dust into the camera (and lenses).

#### 6.3.8. Mechanical loads

Avoid excessive shaking, throwing, dropping or any kind of mishandling of the device.

#### 6.3.9. Camera / lens cleaning

Please follow instructions described below.

- Use only optical quality tissue / cloth (dry cotton) a standard camera lens cleaning kit, if you must clean a lens or filter. Do
  not apply excessive force.
- Use only optics cleaner (e.g. 60% ethyl alcohol, 40% ether). Never use aggressive cleaners like gasoline or spirits. Such cleaners may destroy the surface.
- Do not use compressed air.

# 6.3.10. Protect against static discharge (ESD)

Image sensors and the PCB are easily damaged by static discharge (ESD).

- Please use anti-static gloves, clothes and materials. Also use conductive shoes.
- wear an ESD protection wrist strap.
- Install a conductive mat on the floor and / or working table to prevent the generation of static electricity.

## 6.3.11. Safety instructions for board level cameras

Abuse or misapplication of the board level camera may result in limited warranty or cancelation of warranty. Due to the exposed electronics, special rules apply:

- Only qualified personnel are allowed to handle, install and operate the board level cameras.
- Board level cameras are delivered without housing. Handle the PCB and the sensor with care. Do not bend the boards. Do
  not touch the components or contacts on a board. Hold the board by its edges only.
- Protect the board level camera against static discharge (see 6.3.10 Protect against static discharge (ESD)).
- Do not hold any components of your board level cameras against your clothing, even if you are wearing a wrist strap.
- Do not remove the board level camera from its anti-static packaging unless your body is grounded.
- To protect the boards from radiation of other modules or devices a housing or shielding may be required.
- Be sure that the board level camera has no contact to any electrical source before mounting or making connections to the board level camera.
- Do not connect or disconnect any cables or use the board level camera during an electrical storm.
- Avoid any mechanical forces to the board level cameras, especially torsional, tensile and compressive forces. Any of these
  forces may result in damage of the board level cameras.
- Always use clean boards.
- To protect the boards from dirt like dust or liquids always use the board level cameras in clean room environment or use a protective housing.

# 6.4. Warranty

In addition to the provisions of Article VIII of the Standard Terms & Conditions of XIMEA GmbH (see <u>6.7 Standard Terms & Conditions of XIMEA GmbH</u>) the following additions and specifications apply:

XIMEA warrants to the Original Purchaser that the Camera provided is guaranteed to be free from material and manufacturing defects for a period of two years. Should a unit fail during this period, XIMEA will, at its option, repair or replace the damaged unit. Repaired or replaced Products are covered for the remainder of the original Product warranty period.



Please note our explanations regarding the usage and handling of board level cameras and related restrictions of warranty (see *6.3.11 Safety instructions for board level cameras*).

Warranty is void if any proprietary labeling is removed. This warranty does not apply to units that, after being examined by XIMEA have been found to have failed due to customer abuse, mishandling, alteration, improper installation or negligence. If the original camera module is housed within a case, removing the case for any purpose voids this warranty. This warranty does not apply to damage to any part of the optical path resulting from removal or replacement of the protective glass or filter over the camera, such as scratched glass or sensor damage. If the camera is disassembled, reworked or repaired by anyone other than a recommended service person, XIMEA or its suppliers will take no responsibility for the subsequent performance or quality of the camera.

XIMEA expressly disclaims and excludes all other warranties, express, implied and statutory, including, but without limitation, warranty of merchantability and fitness for a particular application or purpose. In no event shall XIMEA be liable to the Original Purchaser or any third party for direct, indirect, incidental, consequential, special or accidental damages, including without limitation damages for business interruption, loss of profits, revenue, data or bodily injury or death except in case of willful misconduct by XIMEA or employees of XIMEA.

# 6.5. Disclaimer of Warranty

In addition to the provisions of Article XII of the Standard Terms & Conditions of XIMEA GmbH (see <u>6.7 Standard Terms & Conditions of XIMEA GmbH</u>) the following apply:

Although XIMEA has taken care to ensure the accuracy of the information contained herein it accepts no responsibility for the consequences of any use thereof and also reserves the right to change the specification of goods without notice.

XIMEA does not assume any liability for damage that is the result of improper use of its products or failure to comply with the operating manuals or the applicable rules and regulations.

#### 6.6. List Of Trademarks

XIMEA, xiC xiQ, xiMU, xiB, xiB-64, xiX, xSWITCH, xPLATFORM, xEC, xEC2, xiCool, xiRAY, xiCe, xiSpec, xiFLY, xiD, xiJ, xiLAB, xiAPI, xiCamTool, xiCOP and CURRERA are trademarks or registered trademarks of XIMEA GmbH in Germany, Slovakia, USA and other countries.

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#### 6.7. Standard Terms & Conditions of XIMEA GmbH

Printed here for your information is the version from May 2021. Please check the most current version at <a href="http://www.ximea.com/en/corporate/generaltc">http://www.ximea.com/en/corporate/generaltc</a>

**General Conditions** 

for the Supply of Products and Services of the Electrical and Electronics Industry ("Grüne Lieferbedingungen" – GL)\* for commercial transactions between businesses

recommended by ZVEI-Zentralverband Elektrotechnik- und Elektronikindustrie e. V.

as of May 2021 (with 24 months warranty period), and listed below

#### Article I: General Provisions

- 1. Legal relations between Supplier and Purchaser in connection with supplies and/or services of the Supplier (hereinafter referred to as "Supplies") shall be solely governed by the present GL. The Purchaser's general terms and conditions shall apply only if expressly accepted by the Supplier in writing. The scope of delivery shall be determined by the congruent mutual written declarations.
- 2. The Supplier herewith reserves any industrial property rights and/or copyrights and rights of use pertaining to its cost estimates, drawings and other documents (hereinafter referred to as "Documents"). The Documents shall not be made accessible to third parties without the Supplier's prior consent and shall, upon request, be returned without undue delay to the Supplier if the



contract is not awarded to the Supplier. Sentences 1 and 2 shall apply mutatis mutandis to the Purchaser's Documents; these may, however, be made accessible to those third parties to whom the Supplier has rightfully subcontracted Supplies.

- 3. The Purchaser has the non-exclusive right to use standard software and firmware, provided that it remains unchanged, is used within the agreed performance parameters, and on the agreed equipment. Without express agreement the Purchaser may make one back-up copy of standard software.
- 4. Partial deliveries are allowed, unless they are unreasonable to accept for the Purchaser.
- 5. The term "claim for damages" used in the present GL also includes claims for indemnification for useless expenditure.

#### Article II: Prices, Terms of Payment, and Set-Off

- 1. Prices are ex works and excluding packaging; value added tax shall be added at the then applicable rate.
- 2. If the Supplier is also responsible for assembly or erection and unless otherwise agreed, the Purchaser shall pay the agreed remuneration and any incidental costs required, e. g. for traveling and transport as well as allowances.
- 3. Payments shall be made free Supplier's paying office.
- 4. The Purchaser may set off only those claims which are undisputed or non-appealable.

#### Article III: Retention of Title

- 1. The items pertaining to the Supplies ("Retained Goods") for which the purchase price claim is due immediately or for which a payment period of up to and including 30 days after delivery, delivery with installation/assembly or receipt of invoice has been agreed for the due date of the purchase price claim shall remain the property of the Supplier until payment has been made in total.
- 2. In all other cases, the items pertaining to the Supplies ("Retained Goods") shall remain the Supplier's property until each and every claim the Supplier has against the Purchaser on account of the business relationship has been fulfilled. If the combined value of the Supplier's security interests exceeds the value of all secured claims by more than 20 %, the Supplier shall release a corresponding part of the security interest if so requested by the Purchaser; the Supplier shall be entitled to choose which security interest it wishes to release.
- 3. For the duration of the retention of title, the Purchaser may not pledge the Retained Goods or use them as security, and resale shall be possible only for resellers in the ordinary course of their business and only on condition that the reseller receives payment from its customer or makes the transfer of property to the customer dependent upon the customer fulfilling its obligation to effect payment.
- 4. Should Purchaser resell Retained Goods, it assigns to the Supplier, already today, all claims it will have against its customers out of the resale, including any collateral rights and all balance claims, as security, without any subsequent declarations to this effect being necessary. If the Retained Goods are sold on together with other items and no individual price has been agreed with respect to the Retained Goods, Purchaser shall assign to the Supplier such fraction of the total price claim as is attributable to the price of the Retained Goods invoiced by Supplier.
- 5. (a) Purchaser may process, amalgamate or combine Retained Goods with other items. Processing is made for Supplier. Purchaser shall store the new item thus created for Supplier, exercising the due care of a diligent business person. The new items are considered as Retained Goods.
- (b) Already today, Supplier and Purchaser agree that if Retained Goods are combined or amalgamated with other items that are not the property of Supplier, Supplier shall acquire co-ownership in the new item in proportion of the value of the Retained Goods combined or amalgamated to the other items at the time of combination or amalgamation. In this respect, the new items are considered as Retained Goods.
- (c) The provisions on the assignment of claims according to No. 4 above shall also apply to the new item. The assignment, however, shall only apply to the amount corresponding to the value invoiced by Supplier for the Retained Goods that have been processed, combined or amalgamated.
- (d) Where Purchaser combines Retained Goods with real estate or movable goods, it shall, without any further declaration being necessary to this effect, also assign to Supplier as security its claim to consideration for the combination, including all collateral rights for the prorate amount of the value the combined Retained Goods have on the other combined items at the time of the combination.
- 6. Until further notice, Purchaser may collect assigned claims relating to the resale. Supplier is entitled to withdraw Purchaser's permission to collect funds for good reason, including, but not limited to delayed payment, suspension of payments, start of insolvency proceedings, protest or justified indications for overindebtedness or pending insolvency of Purchaser. In addition, Supplier may, upon expiry of an adequate period of notice disclose the assignment, realize the claims assigned and demand that Purchaser informs its customer of the assignment.
- 7. The Purchaser shall inform the Supplier forthwith of any seizure or other act of intervention by third parties. If a reasonable interest can be proven, Purchaser shall, without undue delay, provide Supplier with the information and/or Documents necessary to assert the claims it has against its customers.



8. Where the Purchaser fails to fulfill its duties, fails to make payment due, or otherwise violates its obligations the Supplier shall be entitled to rescind the contract and take back the Retained Goods in the case of continued failure following expiry of a reasonable remedy period set by the Supplier; the statutory provisions providing that a remedy period is not needed shall be unaffected. The Purchaser shall be obliged to return the Retained Goods. The fact that the Supplier takes back Retained Goods and/or exercises the retention of title, or has the Retained Goods seized, shall not be construed to constitute a rescission of the contract, unless the Supplier so expressly declares.

#### Article IV: Time for Supplies; Delay

- 1. Times set for Supplies shall only be binding if all Documents to be furnished by the Purchaser, necessary permits and approvals, especially concerning plans, are received in time and if agreed terms of payment and other obligations of the Purchaser are fulfilled. If these conditions are not fulfilled in time, times set shall be extended reasonably; this shall not apply if the Supplier is responsible for the delay.
- 2. If non-observance of the times set is due to:
- (a) force majeure, such as mobilization, war, terror attacks, rebellion or similar events (e. g. strike or lockout);
- (b) virus attacks or other attacks on the Supplier's IT systems occurring despite protective measures were in place that complied with the principles of proper care;
- (c) hindrances attributable to German, US or otherwise applicable national, EU or international rules of foreign trade law or to other circumstances for which Supplier is not responsible; or
- (d) the fact that Supplier does not receive its own supplies in due time or in due form such times shall be extended accordingly.
- 3. If the Supplier is responsible for the delay (hereinafter referred to as "Delay") and the Purchaser has demonstrably suffered a loss therefrom, the Purchaser may claim a compensation as liquidated damages of 0.5 % for every completed week of Delay, but in no case more than a total of 5 % of the price of that part of the Supplies which due to the Delay could not be put to the intended use.
- 4. Purchaser's claims for damages due to delayed Supplies as well as claims for damages in lieu of performance exceeding the limits specified in No. 3 above are excluded in all cases of delayed Supplies, even upon expiry of a time set to the Supplier to effect the Supplies. This shall not apply in cases of liability based on intent, gross negligence, or due to loss of life, bodily injury or damage to health. Rescission of the contract by the Purchaser based on statute is limited to cases where the Supplier is responsible for the delay. The above provisions do not imply a change in the burden of proof to the detriment of the Purchaser.
- 5. At the Supplier's request, the Purchaser shall declare within a reasonable period of time whether it, due to the delayed Supplies, rescinds the contract or insists on the delivery of the Supplies.
- 6. If dispatch or delivery, due to Purchaser's request, is delayed by more than one month after notification of the readiness for dispatch was given, the Purchaser may be charged, for every additional month commenced, storage costs of 0.5 % of the price of the items of the Supplies, but in no case more than a total of 5 %. The parties to the contract may prove that higher or, as the case may be, lower storage costs have been incurred.

#### Article V: Passing of Risk

- 1. Even where delivery has been agreed freight free, the risk shall pass to the Purchaser as follows:
- (a) if the delivery does not include assembly or erection, at the time when it is shipped or picked up by the carrier. Upon the Purchaser's request, the Supplier shall insure the delivery against the usual risks of transport at the Purchaser's expense;
- (b) if the delivery includes assembly or erection, at the day of taking over in the Purchaser's own works or, if so agreed, after a successful trial run.
- 2. The risk shall pass to the Purchaser if dispatch, delivery, the start or performance of assembly or erection, the taking over in the Purchaser's own works, or the trial run is delayed for reasons for which the Purchaser is responsible or if the Purchaser has otherwise failed to accept the Supplies.

#### Article VI: Assembly and Erection

Unless otherwise agreed in written form, assembly and erection shall be subject to the following provisions:

- 1. Purchaser shall provide at its own expense and in due time:
- (a) all earth and construction work and other ancillary work outside the Supplier's scope, including the necessary skilled and unskilled labor, construction materials and tools;
- (b) the equipment and materials necessary for assembly and commissioning such as scaffolds, lifting equipment and other devices as well as fuels and lubricants;
- (c) energy and water at the point of use including connections, heating and lighting;



- (d) suitable dry and lockable rooms of sufficient size adjacent to the site for the storage of machine parts, apparatus, materials, tools, etc. and adequate working and recreation rooms for the erection personnel, including sanitary facilities as are appropriate in the specific circumstances; furthermore, the Purchaser shall take all measures it would take for the protection of its own possessions to protect the possessions of the Supplier and of the erection personnel at the site;
- (e) protective clothing and protective devices needed due to particular conditions prevailing on the specific site.
- 2. Before the erection work starts, the Purchaser shall unsolicitedly make available any information required concerning the location of concealed electric power, gas and water lines or of similar installations as well as the necessary structural data.
- 3. Prior to assembly or erection, the materials and equipment necessary for the work to start must be available on the site of assembly or erection and any preparatory work must have advanced to such a degree that assembly or erection can be started as agreed and carried out without interruption. Access roads and the site of assembly or erection must be level and clear.
- 4. If assembly, erection or commissioning is delayed due to circumstances for which the Supplier is not responsible, the Purchaser shall bear the reasonable costs incurred for idle times and any additional traveling expenditure of the Supplier or the erection personnel.
- 5. The Purchaser shall attest to the hours worked by the erection personnel towards the Supplier at weekly intervals and the Purchaser shall immediately confirm in written form if assembly, erection or commissioning has been completed.
- 6. If, after completion, the Supplier demands acceptance of the Supplies, the Purchaser shall comply therewith within a period of two weeks. The same consequences as upon acceptance arise if and when the Purchaser lets the two-week period expire or the Supplies are put to use after completion of agreed test phases, if any.

#### Article VII: Receiving Supplies

The Purchaser shall not refuse to receive Supplies due to minor defects.

#### Article VIII: Defects as to Quality

The Supplier shall be liable for defects as to quality ("Sachmängel", hereinafter referred to as "Defects",) as follows:

- 1. Defective parts or defective services shall be, at the Supplier's discretion, repaired, replaced or provided again free of charge, provided that the reason for the Defect had already existed at the time when the risk passed.
- 2. Claims for repair or replacement are subject to a statute of limitations of 24 months calculated from the start of the statutory statute of limitations; the same shall apply mutatis mutandis in the case of rescission and reduction. This shall not apply:
- where longer periods are prescribed by law according to Sec. 438 para. 1 No. 2 (buildings and things used for a building), and Sec. 634a para. 1 No. 2 (defects of a building) German Civil Code ("Bürgerliches Gesetzbuch"),
- in the case of intent,
- in the case of fraudulent concealment of the Defect or
- non-compliance with guaranteed characteristic ("Beschaffenheitsgarantie").

Claims for the reimbursement of expenses on the part of the Purchaser in accordance with Sec. 445a BGB (entrepreneur's right of recourse) shall likewise be subject to a statute of limitations of 24 months from the start of the statutory statute of limitations, provided the last contract in the supply chain is not a sale of consumer goods. The legal provisions regarding suspension of the statute of limitations ("Ablaufhemmung", "Hemmung") and recommencement of limitation periods shall be unaffected.

- 3. Notifications of Defect by the Purchaser shall be given in written form without undue delay.
- 4. In the case of claims for Defects, the Purchaser may withhold payments to an amount that is in a reasonable proportion to the Defect. The Purchaser has no right to withhold payments to the extent that its claim of a Defect is time-barred. Unjustified notifications of Defect shall entitle the Supplier to demand reimbursement of its expenses by the Purchaser.
- 5. The Supplier shall be given the opportunity to repair or to replace the defective good ("Nacherfüllung") within a reasonable period of time.
- 6. If repair or replacement is unsuccessful, the Purchaser is entitled to rescind the contract or reduce the remuneration; any claims for damages the Purchaser may have according to No. 10 shall be unaffected.
- 7. There shall be no claims based on Defect in cases of insignificant deviations from the agreed quality, of only minor impairment of usability, of natural wear and tear, or damage arising after the passing of risk from faulty or negligent handling, excessive strain, unsuitable equipment, defective civil works, inappropriate foundation soil, or claims based on particular external influences not assumed under the contract, or from non-reproducible software errors. Claims based on defects attributable to improper modifications, installation/removal, or repair work carried out by the Purchaser or third parties and the consequences thereof are likewise excluded.
- 8. The Purchaser shall have no claim with respect to expenses incurred in the course of supplementary performance, to the extent that expenses are increased because the subjectmatter of the Supplies has subsequently been brought to another location than the Purchaser's branch office, unless doing so complies with the normal use of the Supplies. This applies accordingly to



claims for the reimbursement of expenses on the part of the Purchaser in accordance with Sec. 445a BGB (entrepreneur's right of recourse), provided the last contract in the supply chain is not a sale of consumer goods.

- 9. The Purchaser's right of recourse against the Supplier pursuant Sec. 445a BGB (entrepreneur's right of recourse) is limited to cases where the Purchaser has not concluded an agreement with its customers exceeding the scope of the statutory provisions governing claims based on Defects.
- 10. The Purchaser shall have no claim for damages based on Defects. This shall not apply to the extent that a Defect has been fraudulently concealed, the guaranteed characteristics are not complied with, in the case of loss of life, bodily injury or damage to health, and/or intentionally or grossly negligent breach of contract on the part of the Supplier. The above provisions do not imply a change in the burden of proof to the detriment of the Purchaser. Any other or additional claims of the Purchaser exceeding the claims provided for in this Article VIII, based on a Defect, are excluded.

#### Article IX: Industrial Property Rights and Copyrights; Defects in Title

- 1. Unless otherwise agreed, the Supplier shall provide the Supplies in the country of the place of delivery only, without infringing any third-party industrial property rights and copyrights (hereinafter referred to as "IPR"). If a third party asserts a justified claim against the Purchaser based on an infringement of an IPR by the Supplies made by the Supplier and used in conformity with the contract, the Supplier shall be liable to the Purchaser within the time period stipulated in Article VIII No. 2 as follows:
- (a) The Supplier shall choose whether to acquire, at its own expense, the right to use the IPR with respect to the Supplies concerned or whether to modify the Supplies such that they no longer infringe the IPR or replace them. If this would be impossible for the Supplier under reasonable conditions, the Purchaser may rescind the contract or reduce the remuneration pursuant to the applicable statutory provisions;
- (b) The Supplier's liability to pay damages is governed by Article XII;
- (c) The above obligations of the Supplier shall apply only if the Purchaser (i) immediately notifies the Supplier of any such claim asserted by the third party in written form, (ii) does not concede the existence of an infringement and (iii) leaves any protective measures and settlement negotiations to the Supplier's discretion. If the Purchaser stops using the Supplies in order to reduce the damage or for other good reason, it shall be obliged to point out to the third party that no acknowledgement of the alleged infringement may be inferred from the fact that the use has been discontinued.
- 2. Claims of the Purchaser shall be excluded if it is responsible for the infringement of an IPR.
- 3. Claims of the Purchaser are also excluded if the infringement of the IPR is caused by specifications made by the Purchaser, by a type of use not foreseeable by the Supplier or by the Supplies being modified by the Purchaser or being used together with products not provided by the Supplier.
- 4. In addition, with respect to claims by the Purchaser pursuant to No. 1 a) above, Article VIII Nos. 4, 5, 8, and 9 shall apply mutatis mutandis in the event of an infringement of an IPR.
- 5. Where other defects in title occur, Article VIII shall apply mutatis mutandis.
- 6. Any other claims of the Purchaser against the Supplier or its agents or any such claims exceeding the claims provided for in this Article IX, based on a defect in title, are excluded.

#### Article X: Conditional Performance

- 1. The performance of this contract is conditional upon that no hindrances attributable to German, US or otherwise applicable national, EU or international rules of foreign trade law or any embargos or other sanctions exist.
- 2. The Purchaser shall provide any information and Documents required for export, transport and import purposes.

#### Article XI: Impossibility of Performance; Adaptation of Contract

- 1. To the extent that delivery is impossible, the Purchaser is entitled to claim damages, unless the Supplier is not responsible for the impossibility. The Purchaser's claim for damages is, however, limited to an amount of 10 % of the value of the part of the Supplies which, owing to the impossibility, cannot be put to the intended use. This limitation shall not apply in the case of liability based on intent, gross negligence or loss of life, bodily injury or damage to health; this does not imply a change in the burden of proof to the detriment of the Purchaser. The Purchaser's right to rescind the contract shall be unaffected.
- 2. Where events within the meaning of Article IV No. 2 (a) to (c) substantially change the economic importance or the contents of the Supplies or considerably affect the Supplier's business, the contract shall be adapted taking into account the principles of reasonableness and good faith. To the extent this is not justifiable for economic reasons, the Supplier shall have the right to rescind the contract. The same applies if required export permits are not granted or cannot be used. If the Supplier intends to exercise its right to rescind the contract, it shall notify the Purchaser thereof without undue delay after having realized the repercussions of the event; this shall also apply even where an extension of the delivery period has previously been agreed with the Purchaser.

#### Article XII: Other Claims for Damages

- 1. Unless otherwise provided for in the present GL, the Purchaser has no claim for damages based on whatever legal reason, including infringement of duties arising in connection with the contract or tort.
- 2. This does not apply if liability is based on:
- (a) the German Product Liability Act ("Produkthaftungsgesetz");
- (b) intent;
- (c) gross negligence on the part of the owners, legal representatives or executives;
- (d) fraud;
- (e) failure to comply with a guarantee granted;
- (f) negligent injury to life, limb or health; or
- (g) negligent breach of a fundamental condition of contract ("wesentliche Vertragspflichten").

However, claims for damages arising from a breach of a fundamental condition of contract shall be limited to the foreseeable damage which is intrinsic to the contract, provided that no other of the above case applies.

3. The above provision does not imply a change in the burden of proof to the detriment of the Purchaser.

#### Artikel XIII: Venue and Applicable law

- 1. If the Purchaser is a businessman, sole venue for all disputes arising directly or indirectly out of the contract shall be the Supplier's place of business. However, the Supplier may also bring an action at the Purchaser's place of business.
- 2. This contract and its interpretation shall be governed by German law, to the exclusion of the United Nations Convention on contracts for the International Sale of Goods (CISG).

#### Article XIV: Severability Clause

The legal invalidity of one or more provisions of this Agreement in no way affects the validity of the remaining provisions. This shall not apply if it would be unreasonably onerous for one of the parties to be obligated to continue the contract.

# 6.8. Copyright

All texts, pictures and graphics are protected by copyright and other laws protecting intellectual property. It is not permitted to copy or modify them for trade use or transfer, nor may they be used on websites.

# 6.9. Revision History

| Version | Date       | Notes   |
|---------|------------|---|
| 1.00    | 07/14/2017 | First Release   |
| 1.01    | 10/10/2017 | Corrected typos   |
| 1.02    | 10/20/2017 | Corrected high-speed IO specification                   |
| 1.03    | 04/08/2018 | Corrected power consumption values                      |
| 1.04    | 26/06/2019 | Updated certification                                   |
| 1.05    | 06/09/2019 | Formal updates and synchronization with directives.     |
| 1.06    | 01/08/2024 | Corrected information about AR coating of IR650 filters |



# 7. Glossary

| Term /Abbreviation | Definition  |
|--------------------|---|
| ADC                | Analog to Digital Converter                                 |
| API                | Application Programming Interface                           |
| AR (coating)       | Anti-Reflex   |
| B/W or B&W         | Black and White   |
| CCD                | Charge-Coupled Device                                       |
| CDS                | Correlated double sampling                                  |
| CMOS               | Complementary Metal Oxide Semiconductor                     |
| DNC                | Do not connect  |
| DSNU               | Dark Signal non-Uniformity                                  |
| DR                 | Dynamic Range   |
| EMC                | Electro Magnetic Compatibility                              |
| ERS                | Electronic rolling shutter                                  |
| FPN                | Fixed pattern noise   |
| FPS                | Frame per second  |
| FWC                | Full Well Capacity  |
| GR                 | Global reset  |
| GS                 | Global shutter  |
| IR                 | Infra-Red   |
| JTAG               | Joint Test Action Group                                     |
| LSB                | Least Significant Bit                                       |
| MIMR               | Multiple integration multiple ROI                           |
| MSB                | Most significant bit  |
| MSL                | Moisture sensitivity level                                  |
| NA                 | Not Available   |
| PCB                | Printed Circuit Board (same as PWB)                         |
| PGA                | Programmable gain amplifier                                 |
| PRNU               | Photo response non-uniformity                               |
| PWB                | Printed Wiring Board (same as PCB)                          |
| RGB                | Red Green Blue  |
| ROI                | Region of interest  |
| Sat                | Saturation value  |
| SDK                | Software Development Kit                                    |
| SIMR               | Single integration multiple ROI                             |
| SNR                | Signal To Noise (ratio)                                     |
| SPI                | Serial peripheral interface                                 |
| SW                 | Software  |
| TBD                | To be determined – some parameters require characterization |
| Tint               | Integration time  |



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